

GIGABYTE GA-8ID2003-P Schematics

Revision 2.01

SHEET	TITLE
01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	WILLIAMATE_478A
04	WILLIAMATE_478B
05	WILLIAMATE_478C
06	MCH-BROOKDALE-E_A
07	MCH-BROOKDALE-E_B
08	MCH-BROOKDALE-E_C
09	MD0~63
10	DDR1~2
11	DDR-TERM
12	AGP
13	ICH4_1
14	ICH4_2
15	FWH (SINGLE BIOS)
16	ICS950223 CLOCK GEN
17	PCI1_2
18	PCI3_4
19	PCI5
20	ITE LPC I/O
21	IDE
22	KB_PS2

SHEET	TITLE
23	COM_LPT_FDD
24	F-USB1 & F-USB2
25	FPANEL
26	CODEC
27	AUDIO_GAMEPORT
28	AUDIO_SPDIF
29	DDR POWER
30	POWER1
31	POWER2
32	LAN RTL8100B & B-USB CONNECTOR
33	GPIO LIST
34	PCI ROUNT LIST
35	
36	
37	
38	
PCB	十美,育富,群祥,智恩 4v藍c

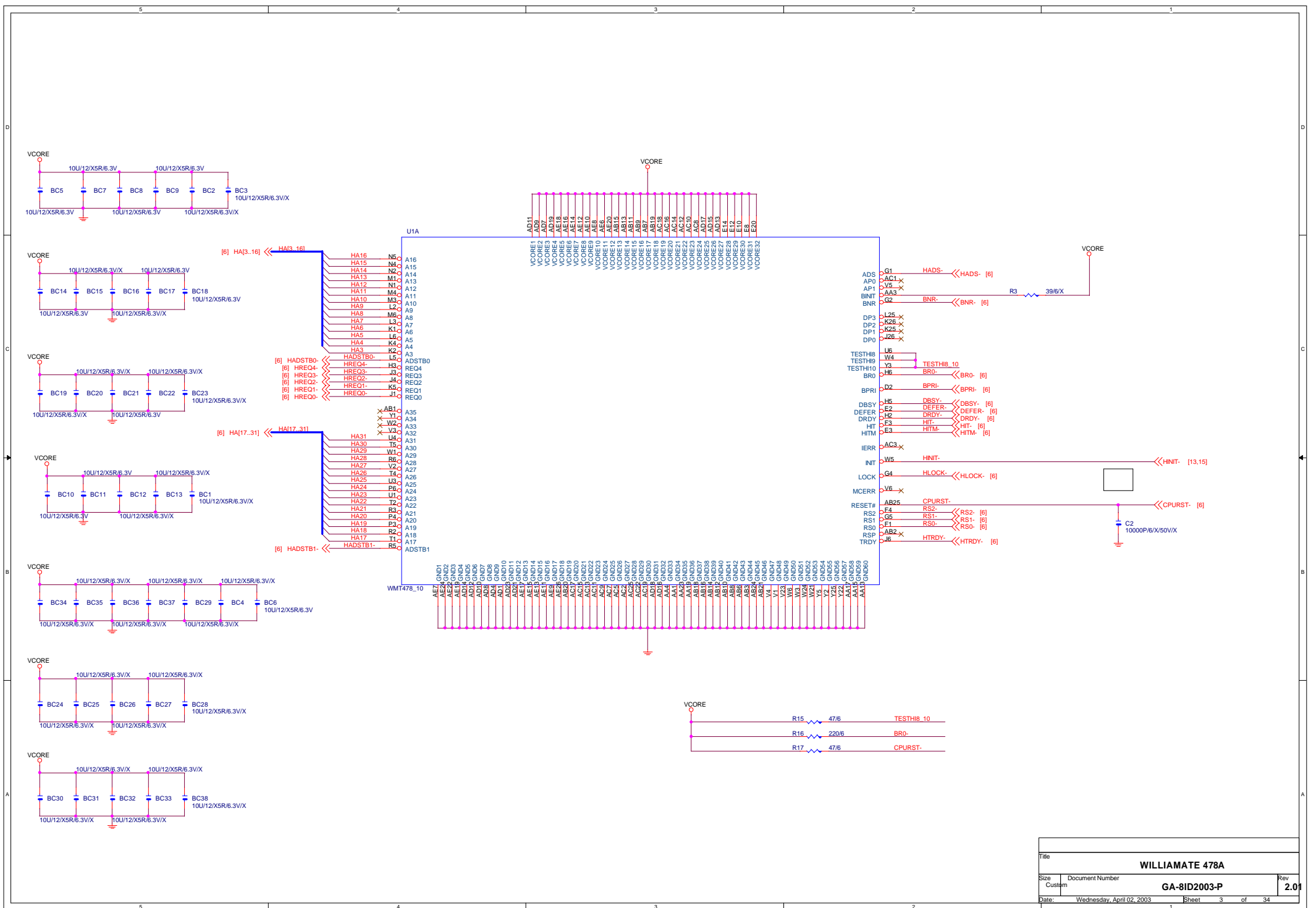
<div>COMPONENT SIDE (0.5 oz. Copper)</div> <div>VCC SIDE (1 oz. Copper)</div> <div>GND SIDE (1 oz. Copper)</div> <div>SOLDER SIDE (0.5 oz. Copper)</div>	
<div>GIGABYTE</div>	
TitleCOVER SHEET	
SizeCustom	Document NumberGA-8ID2003-PRev2.01
DateWednesday, April 02, 2003	Sheet1 of 34

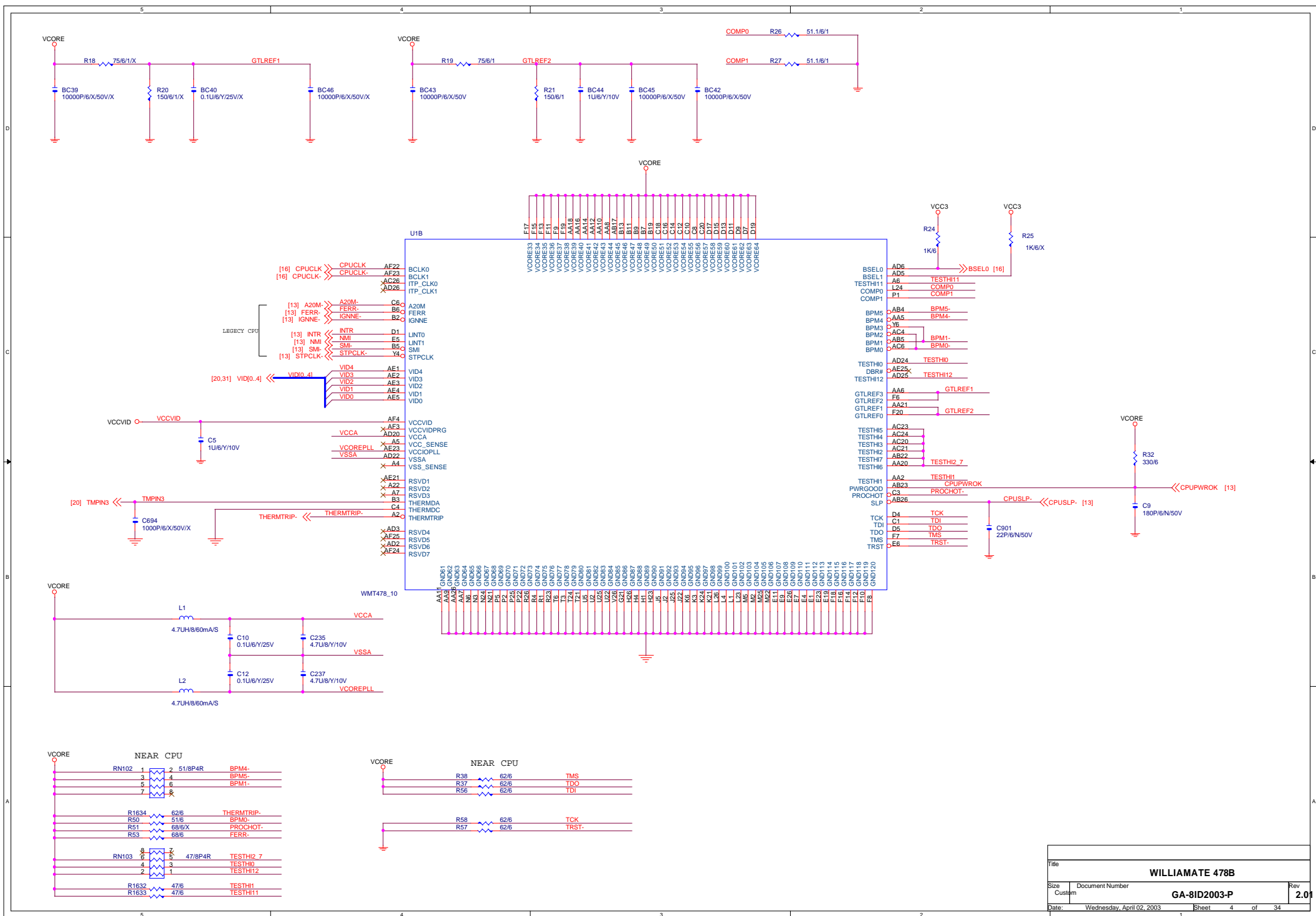
BOM 修改

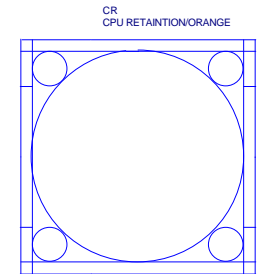
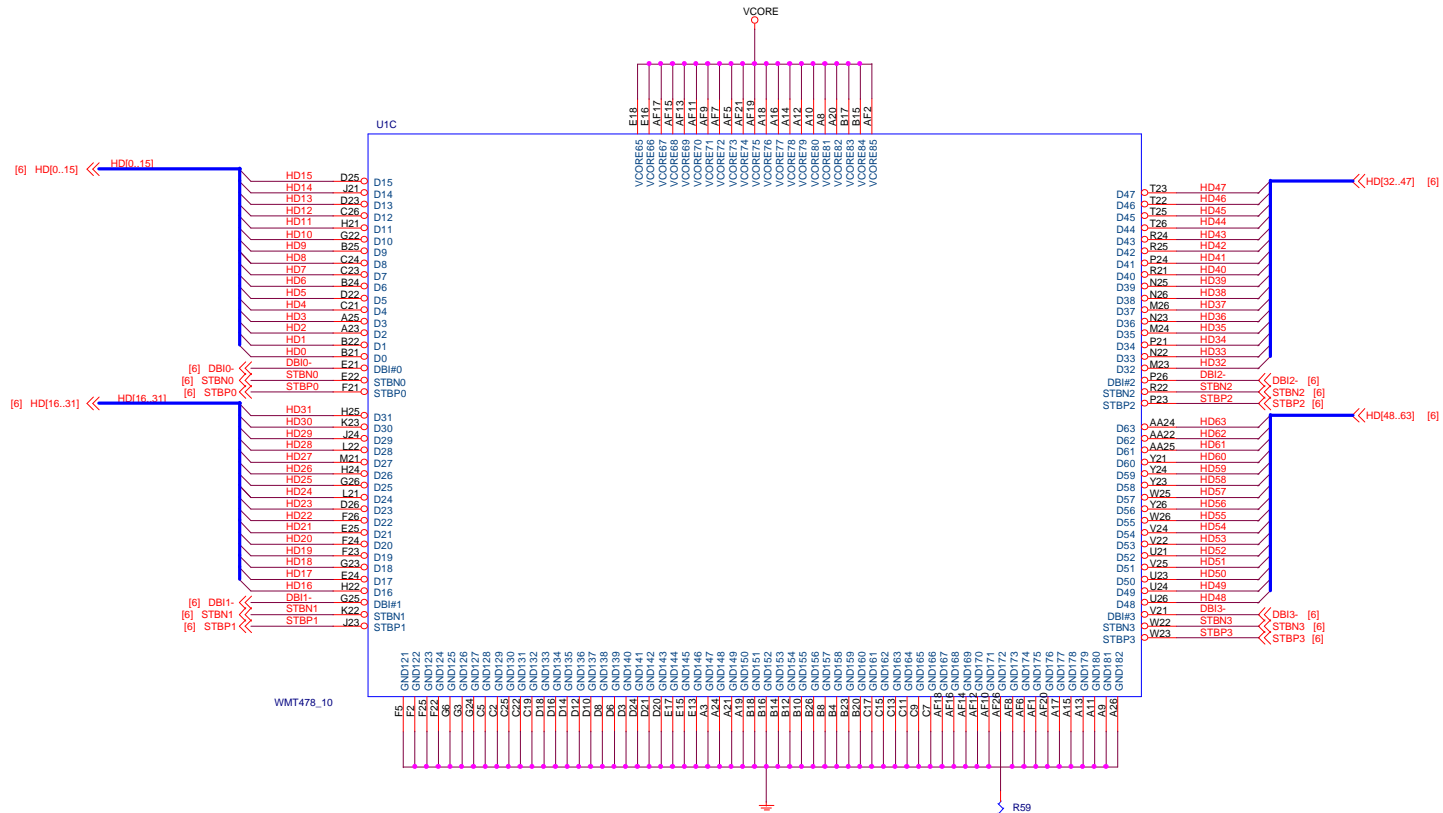
History 版本	修改項目內容	修改原因
0.1-0502	9M8IR-00-10A --> 9M8ID-00-01	參考右邊修改內容
1.0A	9M8IDX --> 9M8ID-00-10A	
1.0B-0607	9M8ID-00-01 --> 9M8ID-00-10B	1. 參考右邊修改內容 2. AUDIO ALC650 --> STAC9721
1.0C-0701	9M8ID-00-10B --> 9M8ID-00-10C	1.FOR SYSTEM SHOWDOWN ISSUE
		R1601 200K --> 240K
1.0A-0718	9M8ID-00-10C --> 9M8ID533-00-10A	PVT
1.1A-0821	9M8ID533-00-10A->9M8ID533-00-11A	1.AUDIO STAC9721 --> ALC201A
		2. CR15 0ohm -->22 ohm
11B-0913	9M8ID533-00-11A --> 9M8ID533-00-11B	1. FOR C-STEPIING CPU HANG
		2.PWM HIGT SIDE MOSFET R1597,R1606 2.2 --> 4.7ohm
11C-1017	9M8ID533-00-11B --> 9M8ID533-00-11C	1. 南橋加替料 U61 : ICH2X SL5PN/C0 (10HB1-032801-A0) 1. 北橋加替料 U24 : 82845D SL5YQ/B0 (10HB1-032845-21)
11C-ECN	9M8ID533-00-11C --> 9M8ID533-00-11C	1. REMOVE 北橋替料 U24 : 82845D SL5YQ/B0 (10HB1-032845-21)
11D	9M8ID533-00-11C --> 9M8ID533-00-11D	1. 北橋加替料 U24 : 82845D SL5YQ/B0 (10HB1-032845-21)
		NET ST0 R67 2K REMOVE , ADD R119 6.8K
		2.REMOVE BIOS WINBOND W49V002FAP 2M
11D-1106	9M8ID533-00-11D --> 9MK8ID533-00-11D	1.9MK = 3MK + 4MK +5MK
11E-1129	9M8ID533-00-11D --> 9M8ID533-00-11E	1. PWM MOSFET REMOVE FDB7030 & FDB306
		2. 1N5817 REMOVE DII
10J-1122	9M8ID533-00-11D --> 9M8ID533-00-11J	1. PCB REV1.1 --> REV1.11
		2. C881 0.1U/6/Y25V --> 1U/8/Y/25V
		3. ADD C920=1U/8/Y/25V , Q67=PMBZ5237B , R1620=10/6
		4. REMOVE D60,D61= 1N5817
11J-1128	9M8ID533-00-11J --> 9M8ID533-00-11J	1.DVT --> MP
11J--ECN-1129	9M8ID533-00-11J --> 9M8ID533-00-11J	1. ADD C900 1U/6
11J-1212	9M8ID533-00-11J --> 9MK8ID533-00-11J	1.9MK = 3MK + 4MK +5MK
		2. DEL COLOR BOX & MANUEL
11J-0106	9M8ID533-00-11J --> 9M8ID533-00-11J	1. ADD BIOS WINBOND W49V002FAP 2M
20A-0113	9M8IR533-00-20A --> 9M8ID2003-00-20A	1. DDR --> SDRAM
20A-0124	9M8IR533-00-20A --> 9M8ID2003-00-20A	1. EC14 1000U/16V/10*16 --> 1000U/16V/8*11.5

PCB version 變更(線路及Layout Change) History

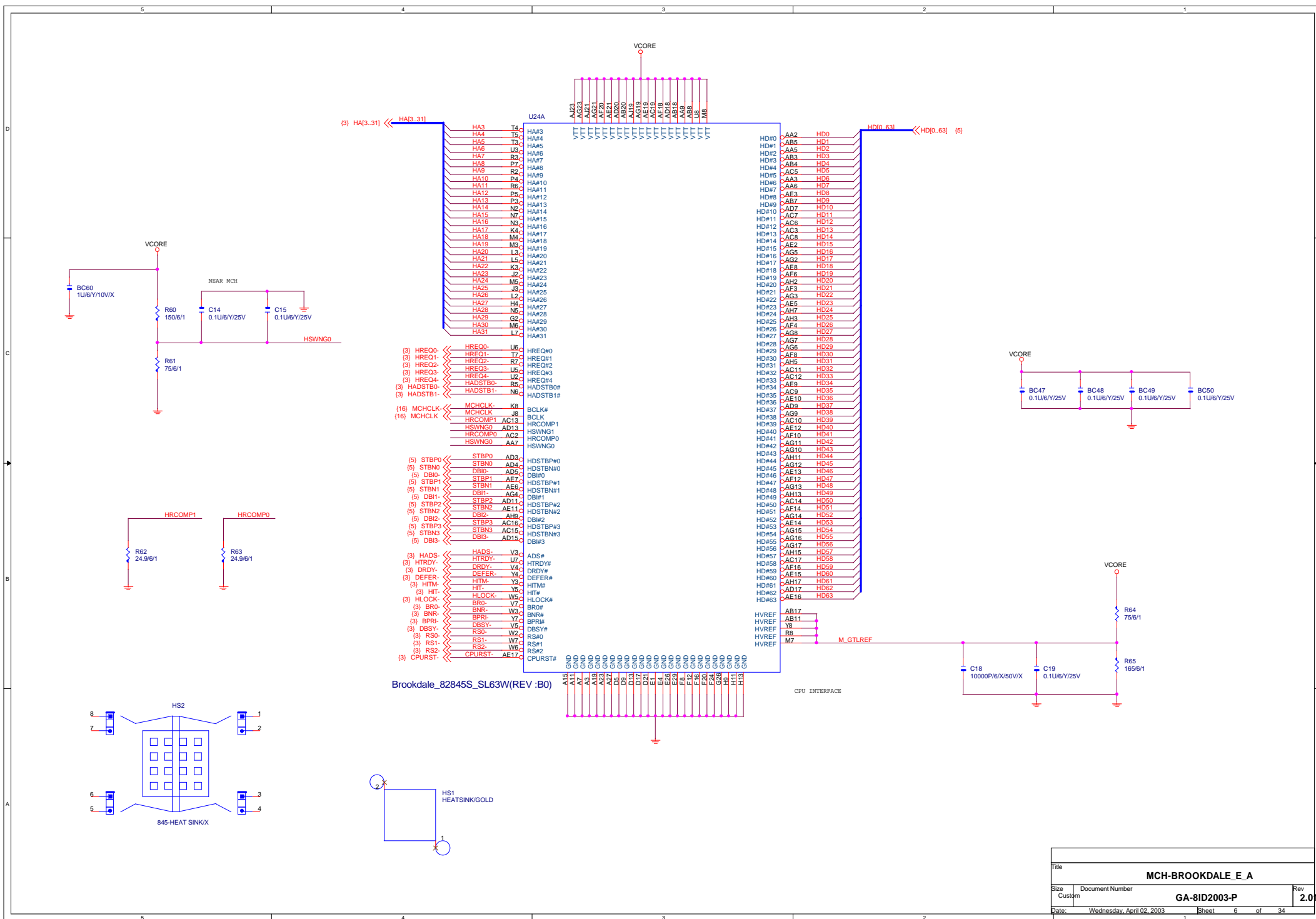
Change History		
Pcb 版本	修改項目內容	修改原因
0.1	8IR REV1.0 --> 8ID REV0.1	
1.0	8ID REV0.1 --> 8ID REV1.0	1. EMI ADD AC97 PIN9 PULL DOWN 2.2ohm
		2.EMI ADD LPC24 CLK 10P
1.0	8ID REV0.1 --> 8ID533 REV1.0	1.TEXT 8ID --< 8ID533
1.1	8ID533 REV1.0 --> 8ID533 REV1.1	1.TEXT REV1.0 --> REV1.1
1.11	8ID533 REV1.1 --> 8ID533 REV1.11	1.D61 1N5817 正端接 C900 1U/6 2. ADD PMBZ5237B CIRCUIT FOR PWM PIN12 BOOTB
2.0	8IR533 REV2.0 --> 8ID533 REV2.0	1. DDR --> SDRAM
2.01	8ID2003 REV2.0 --> 8ID2003 REV2.01	1. SDRAM POWER INPUT VCC3 --> VCC 2. ADD ALC650 VCC3 INPUT 100U
BOM		
20A-0218	9M8ID2003-00-20A --> 9M8ID2003-00-20A	1. Q148,Q151加替料FDB6670AL-MT : 10IF3-806570-10
20A-0226	9M8ID2003-00-20A --> 9M8ID2003P-00-20A	1.PCB REV2.0 --REV2.01 2. AC97 ALC201A --> ALC650
20A-0320	9M8ID2003P-00-20A --> 9M8ID2003P-00-20A	1. MP 2. 1000U電容ESR=98mohm --> 30mohm

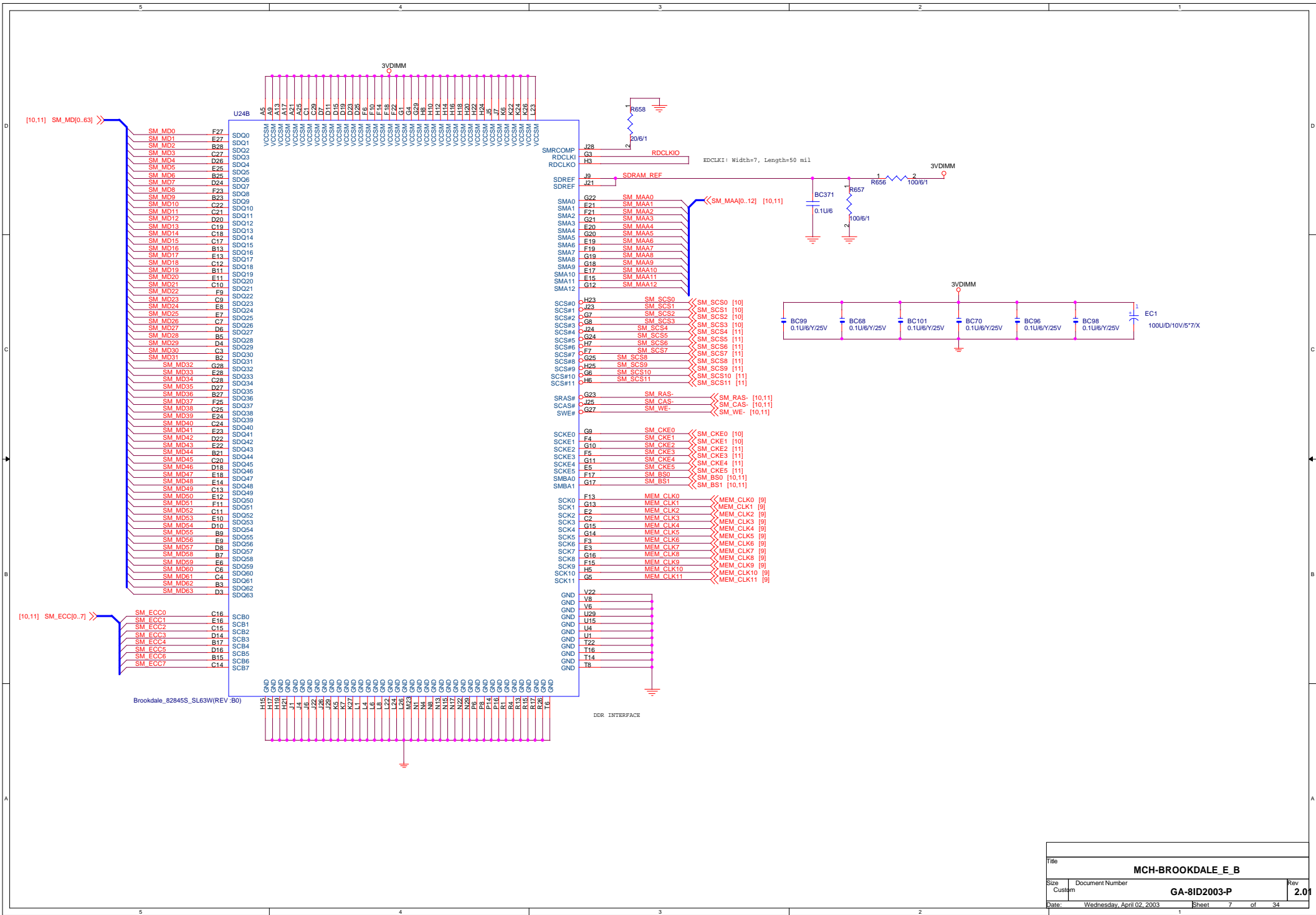


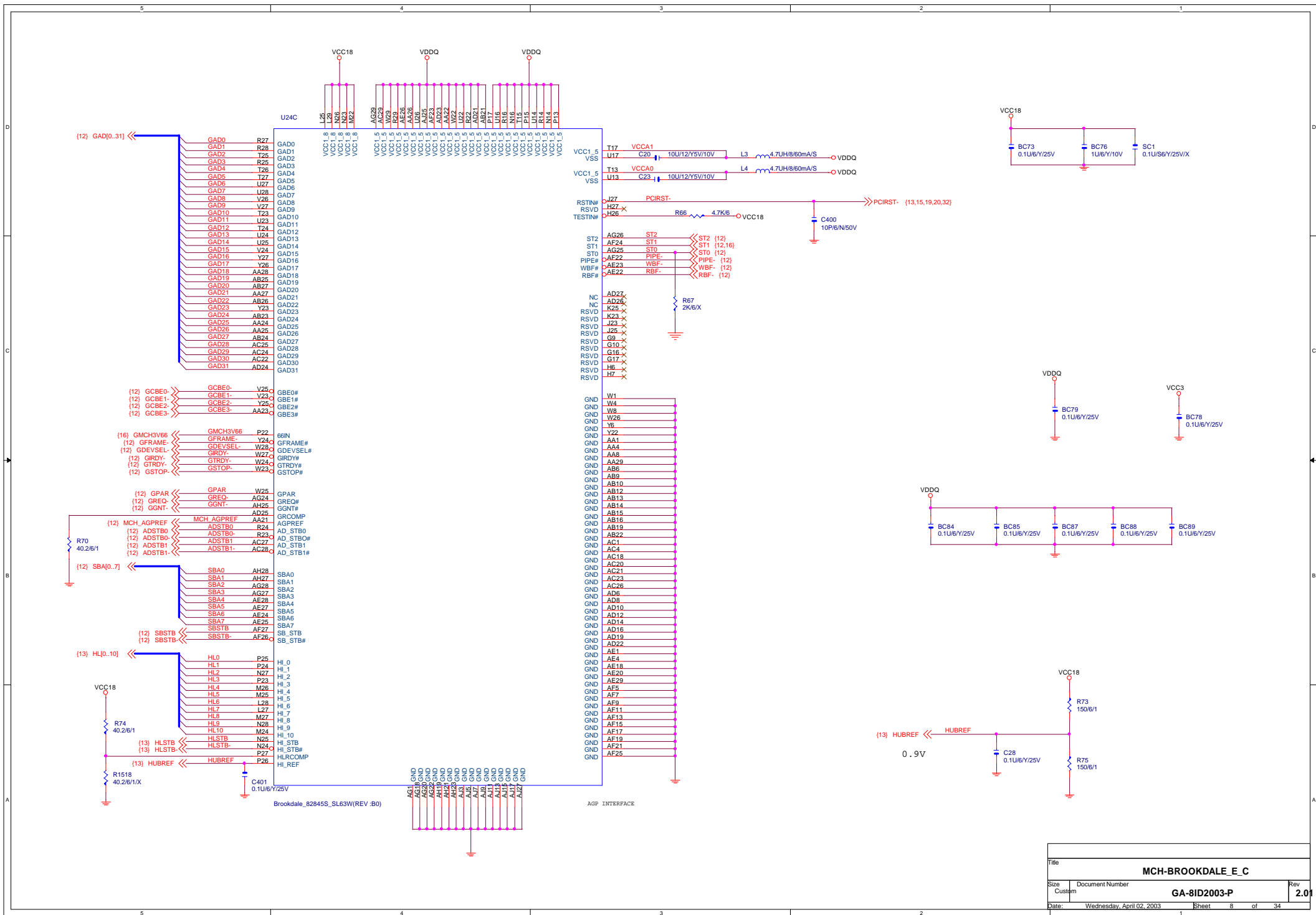




Title		
WILLIAMATE 478C		
Size	Document Number	Rev
Custom	GA-8ID2003-P	2.01
Date:	Wednesday, April 02, 2003	Sheet 5 of 34

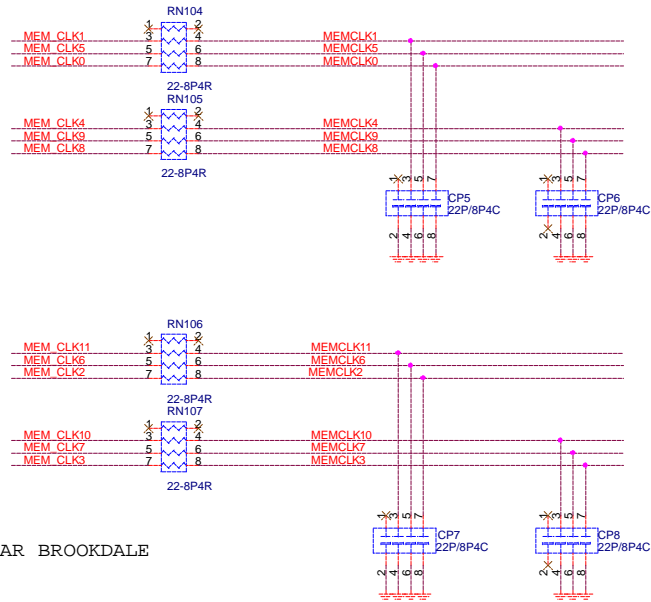






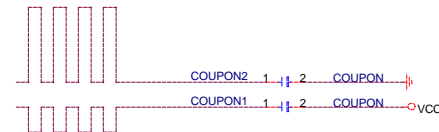
[10,11] MEMCLK[0..11] << MEMCLK10..11

[7] MEM_CLK[0..11] << MEM_CLK10..11

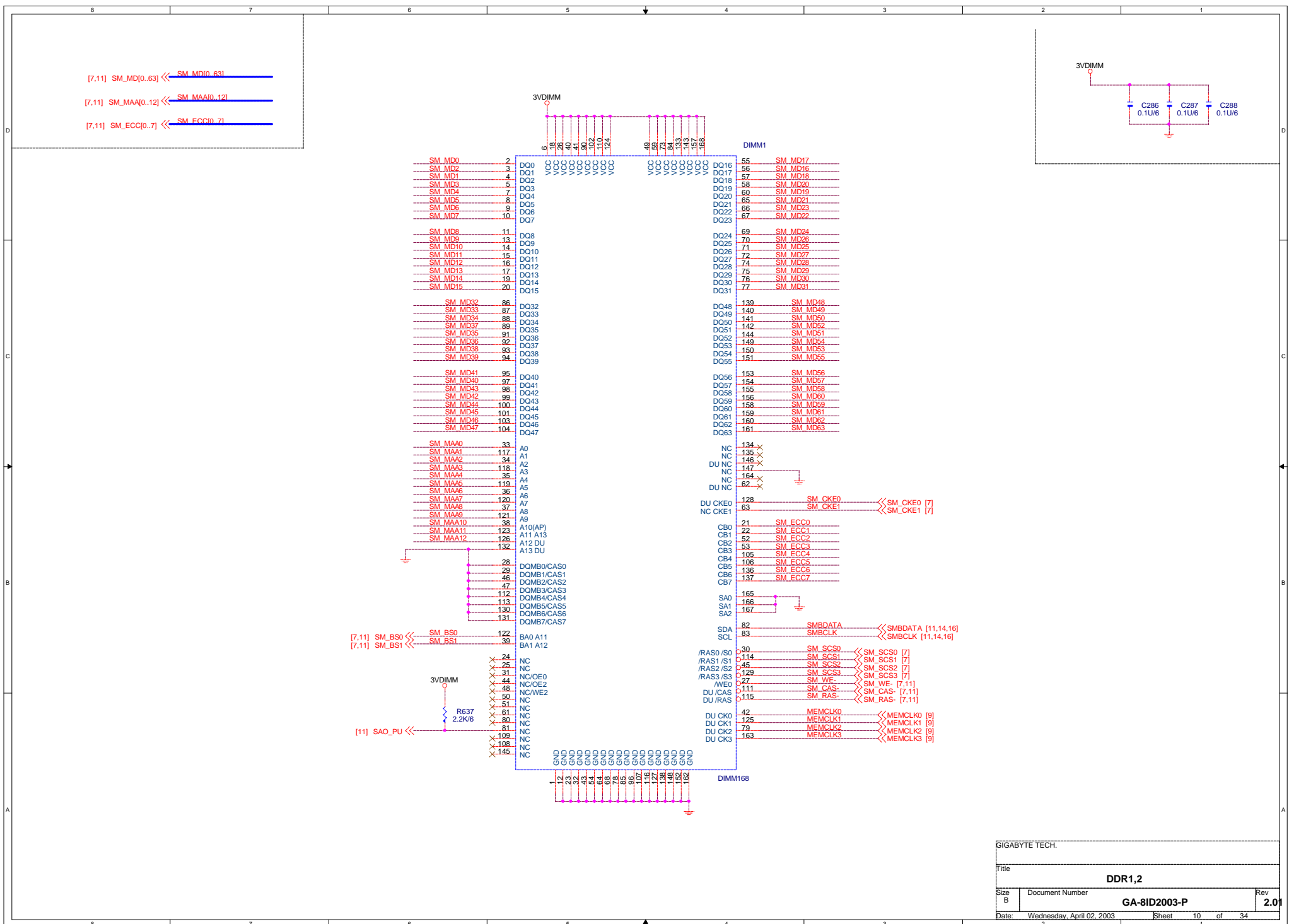


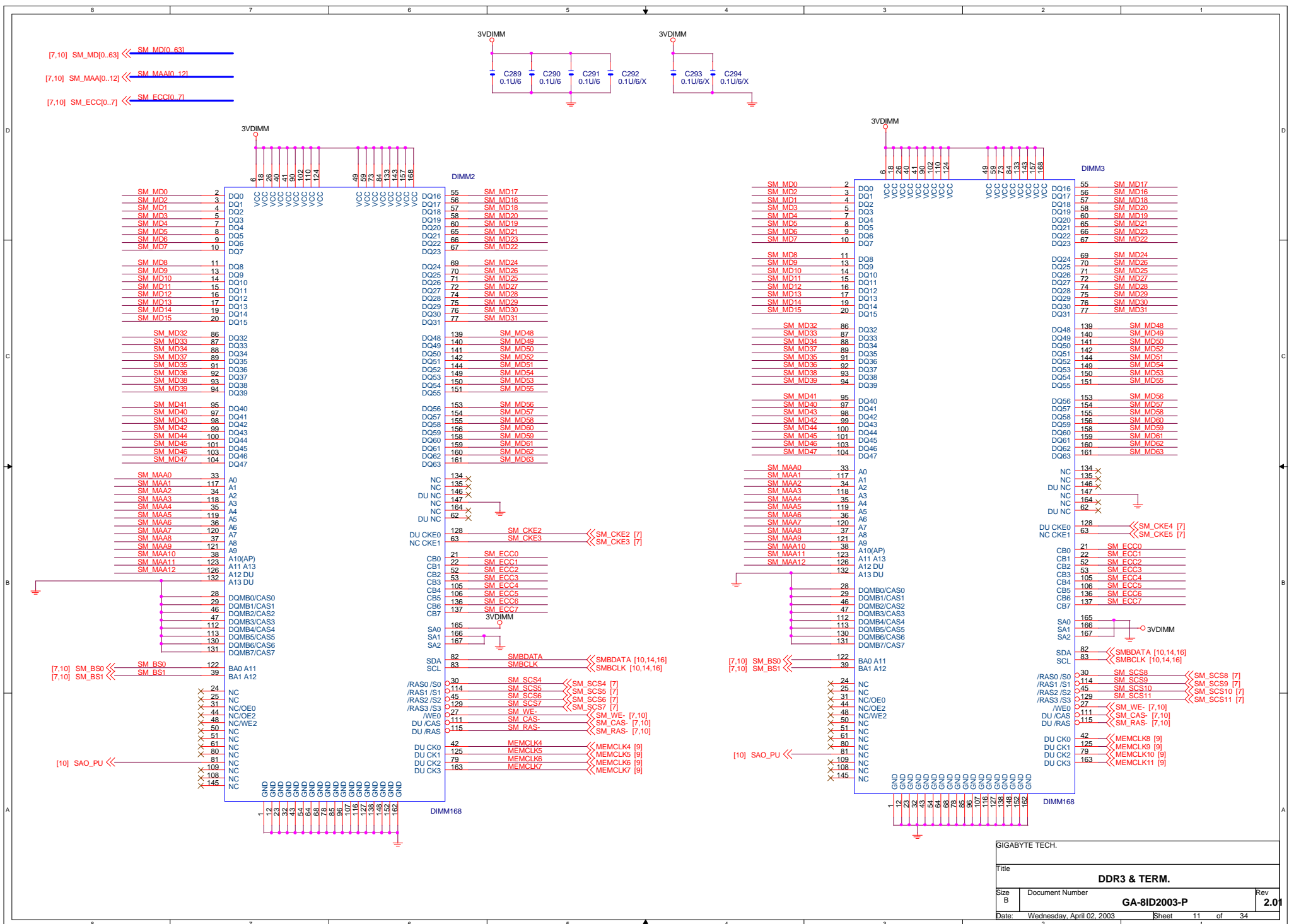
ONLY FOR EMI

NEAR BROOKDALE



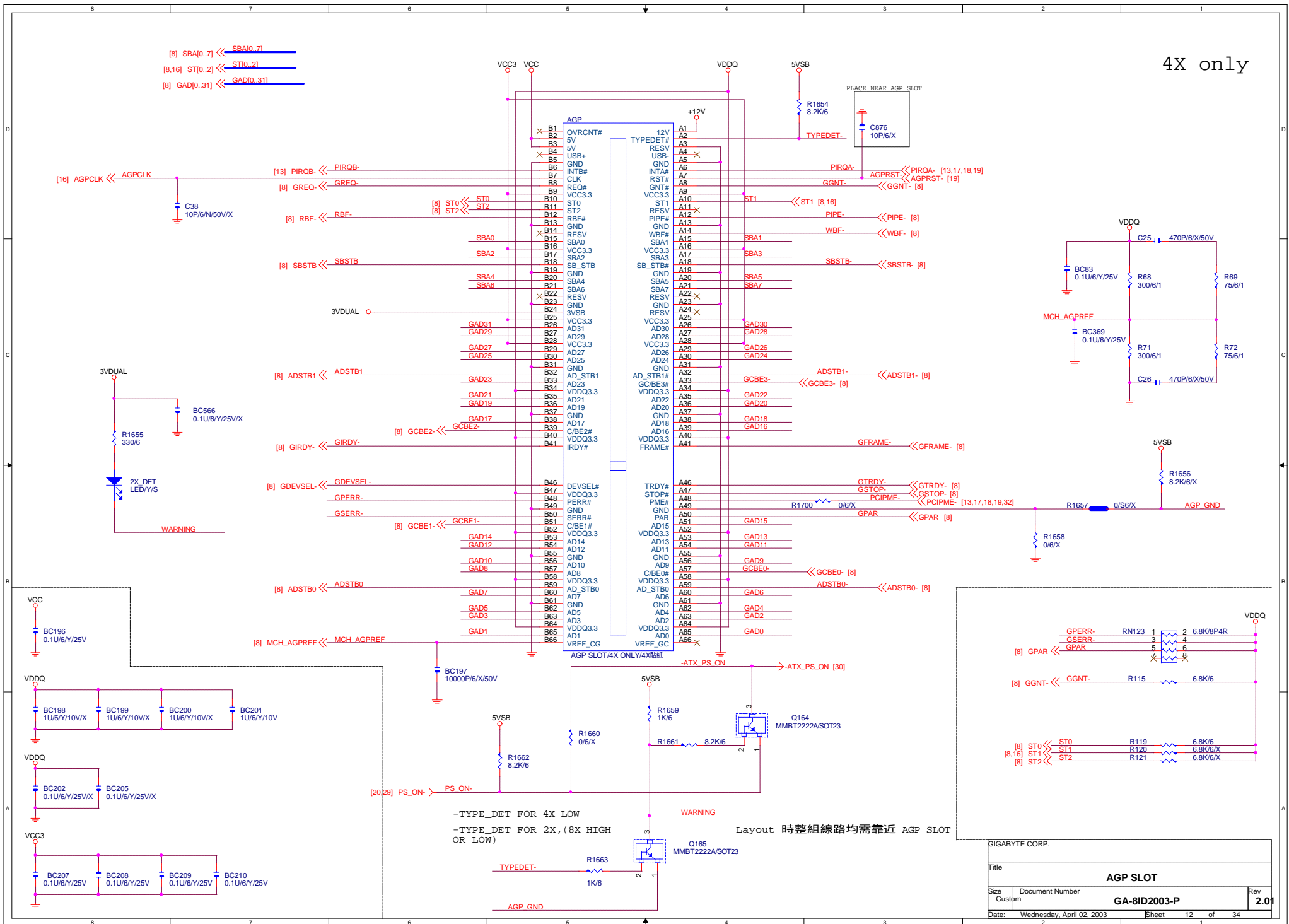
GIGABYTE CORP.			
Title			
MD0_63 SERIAL TERM.			
Size	Document Number		Rev
B	GA-8ID2003-P		2.01
Date:	Wednesday, April 02, 2003	Sheet	9 of 34

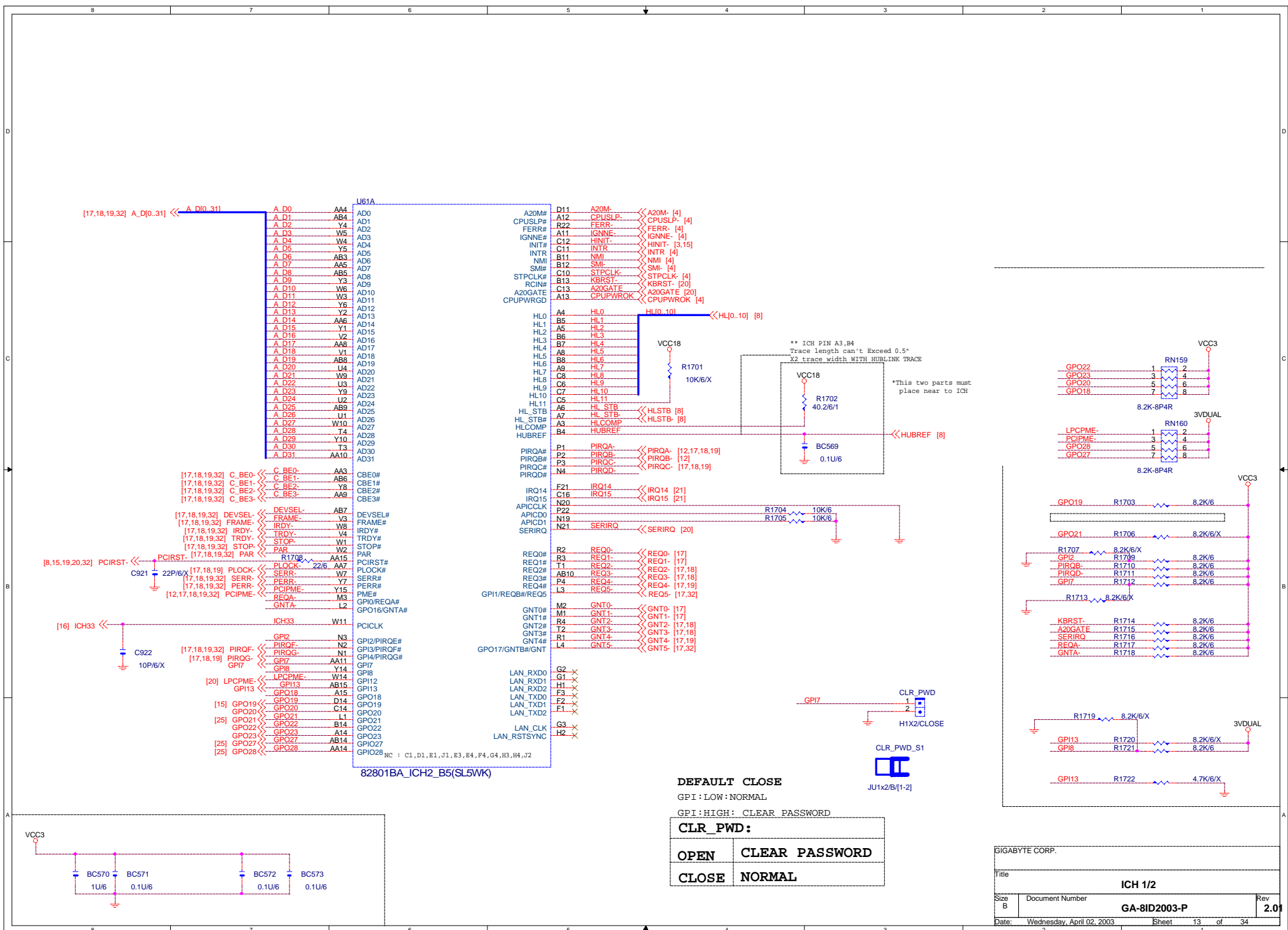


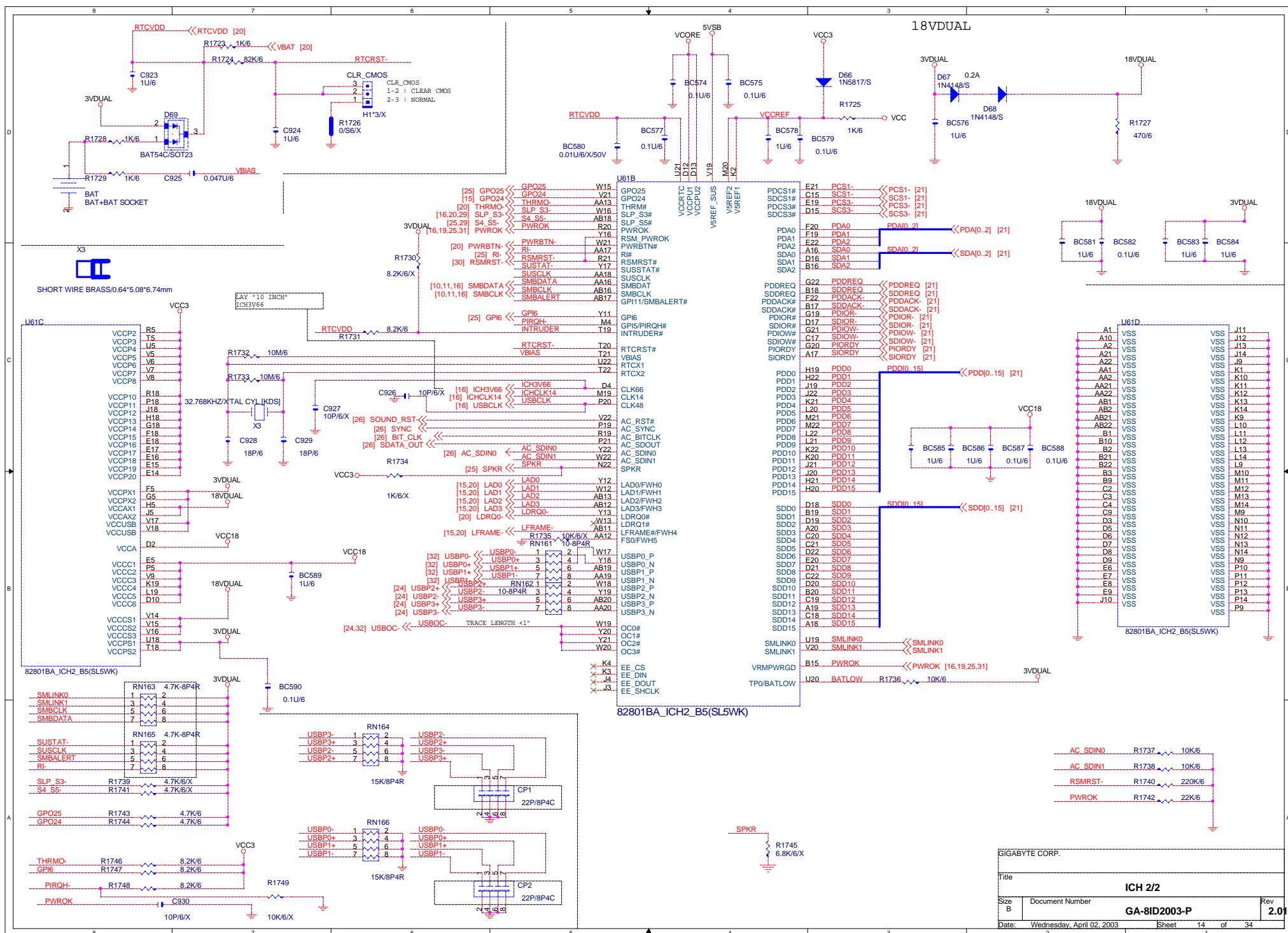


[8] SBA[0..7] << SBA[0..7]
[8,16] ST[0..2] << ST[0..2]
[8] GAD[0..31] << GAD[0..31]

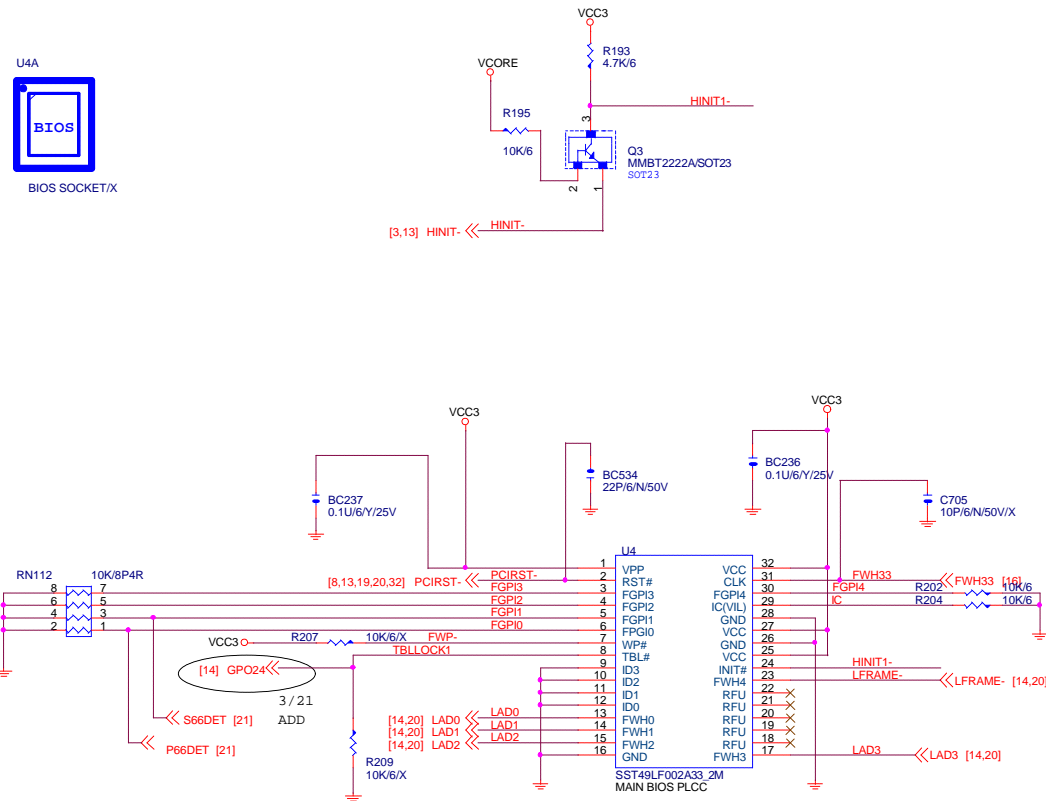
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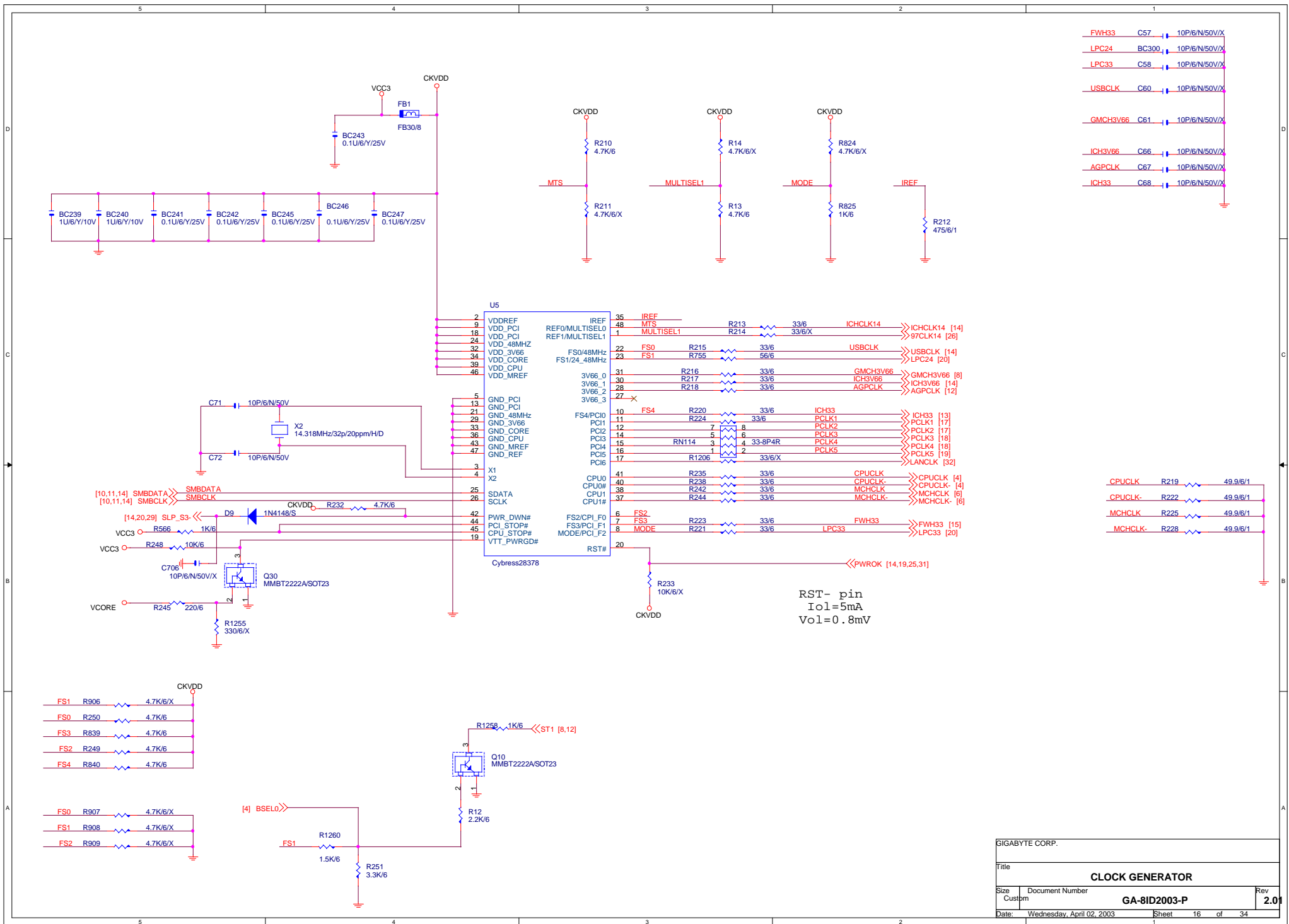


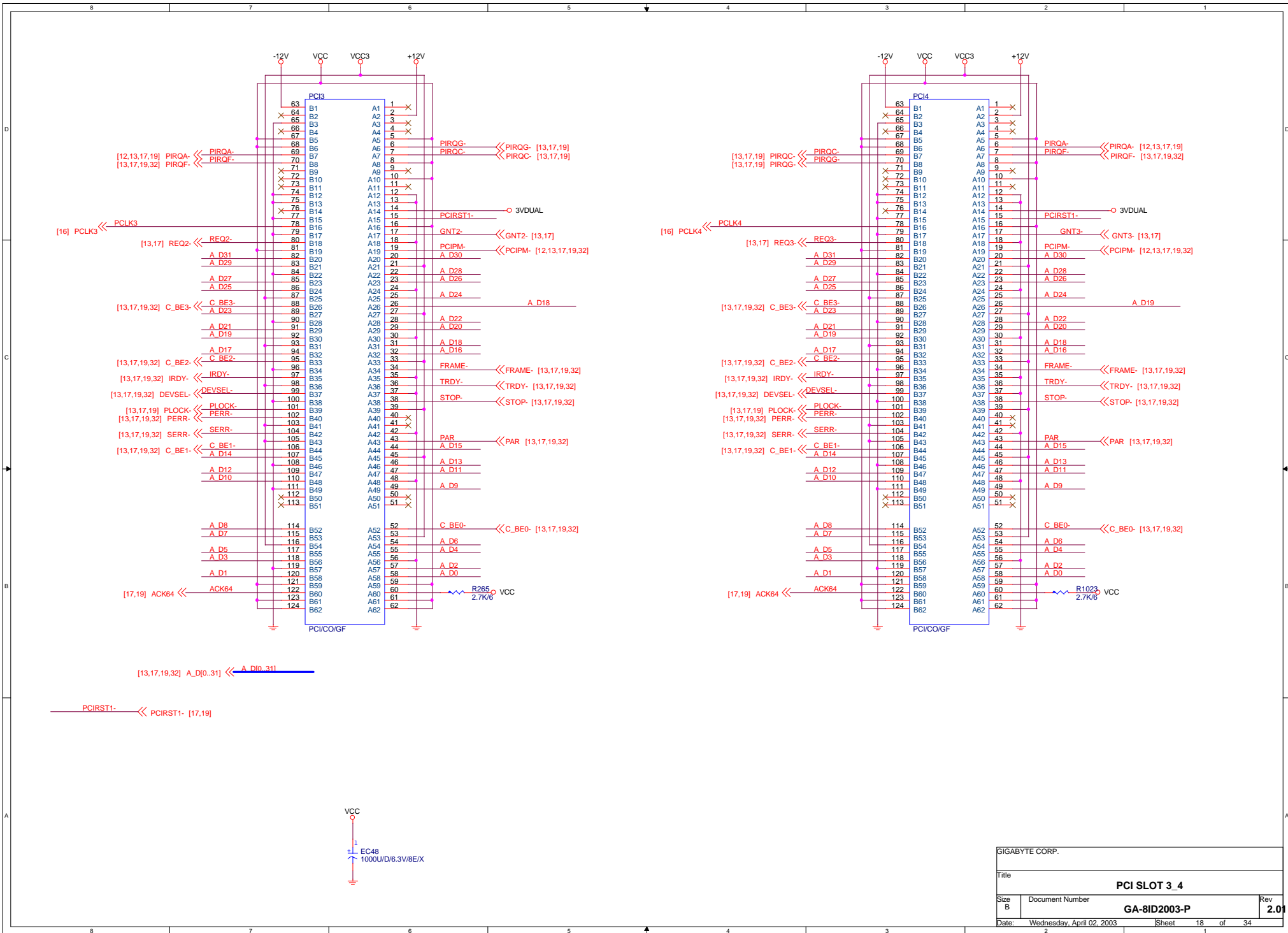


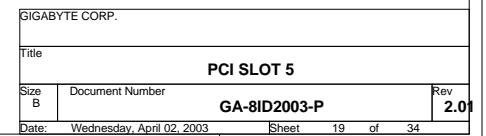
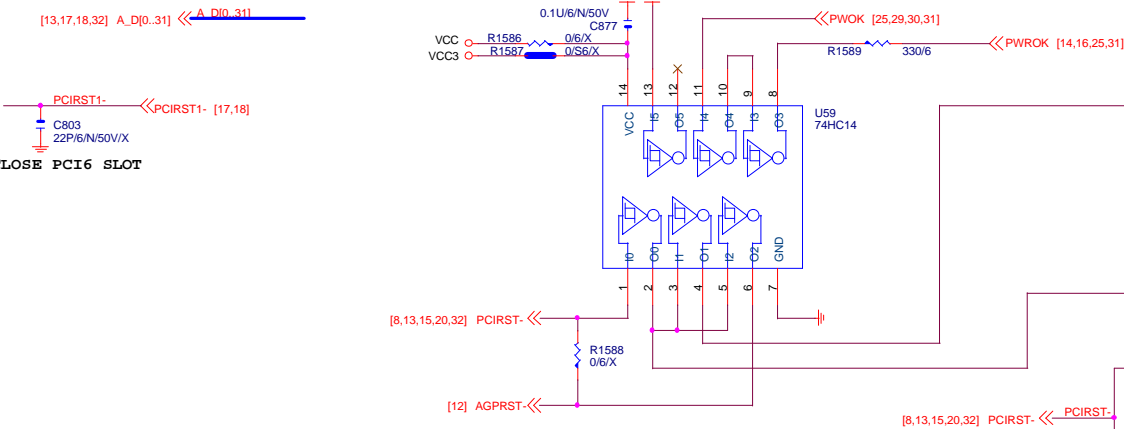


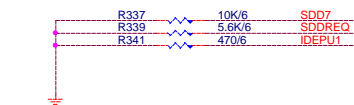
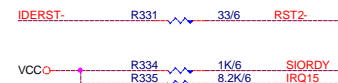
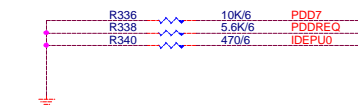
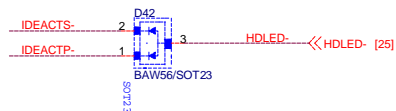
2002/04/15 MODIFY WRITE PROTECT



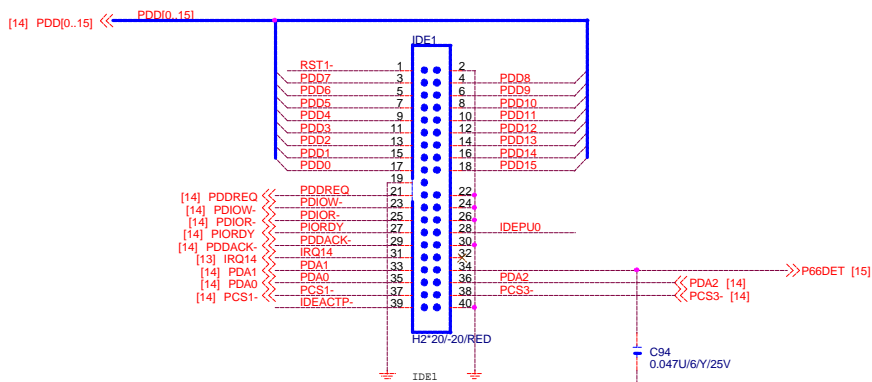




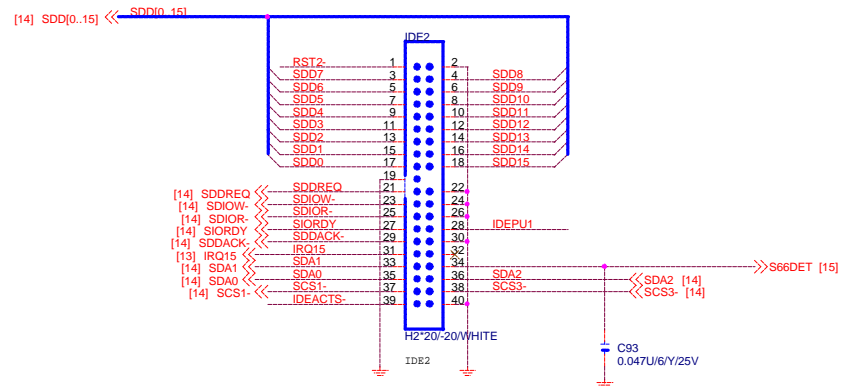




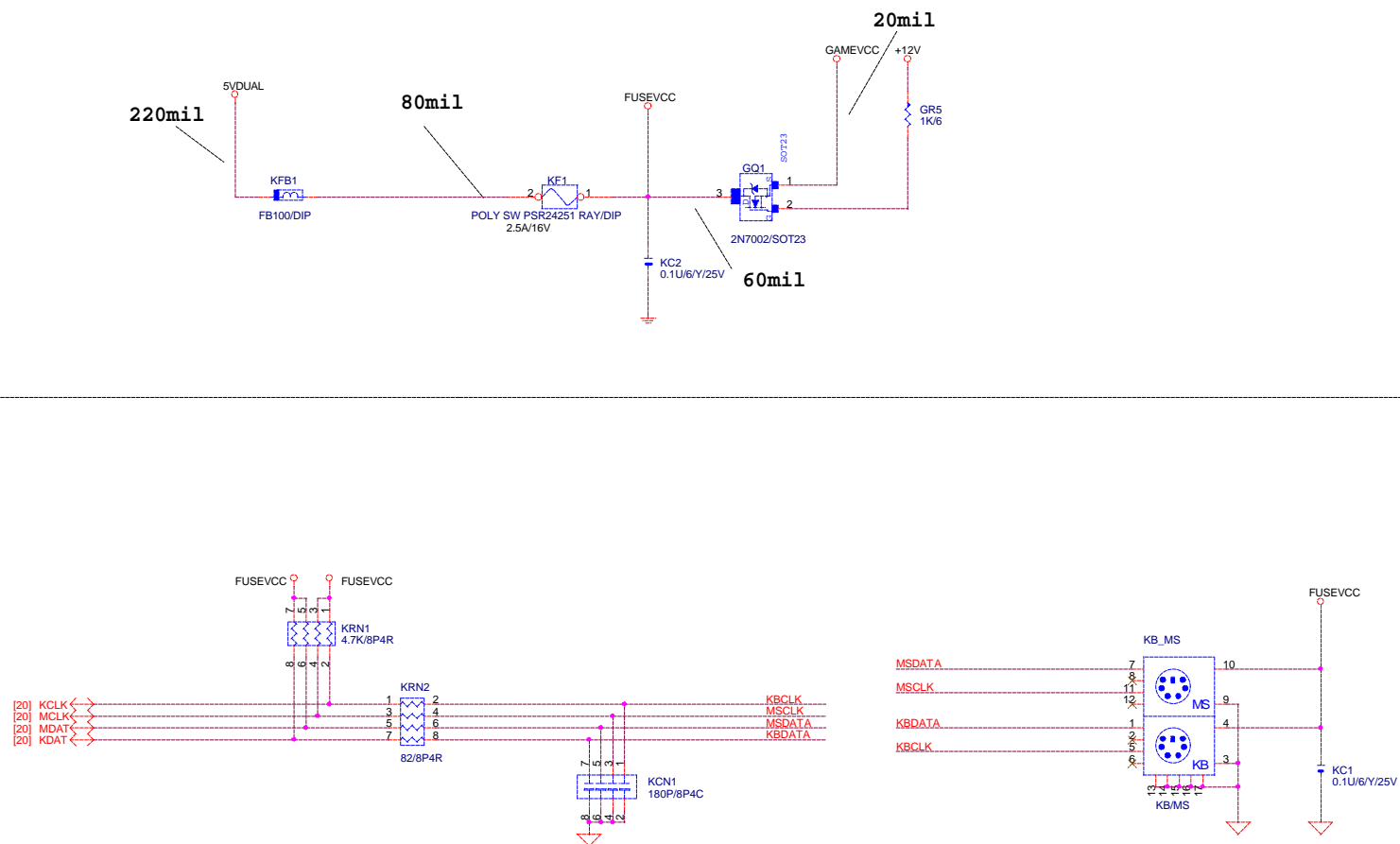
PRIMARY IDE CONNECTOR



SECONDARY IDE CONNECTOR

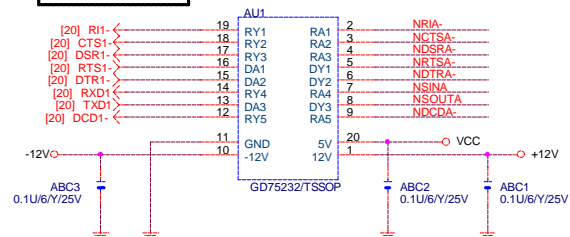


模組化線路

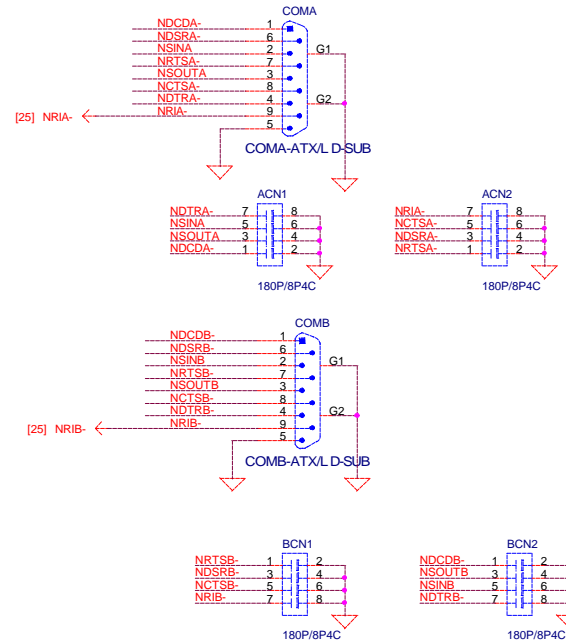
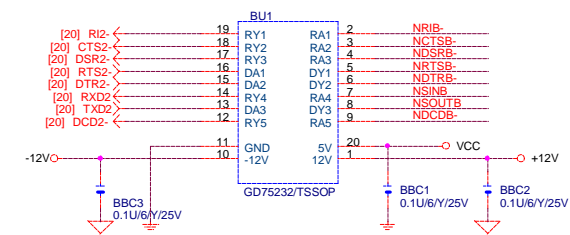


COMA

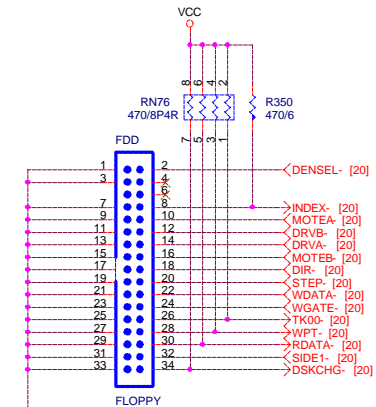
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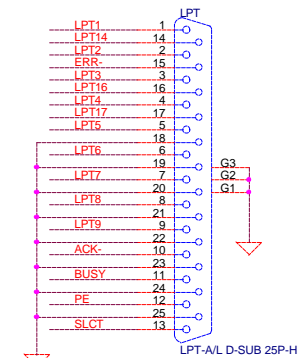
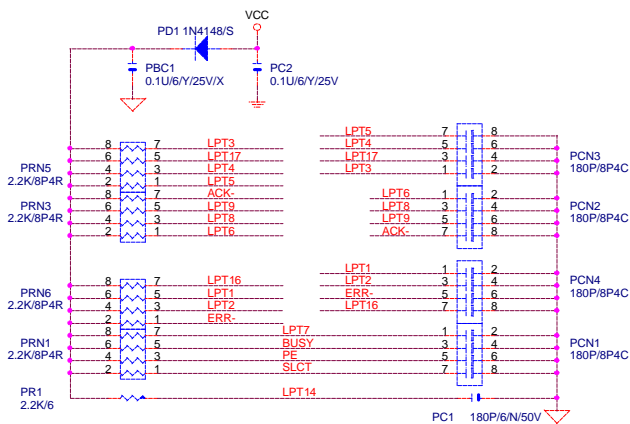
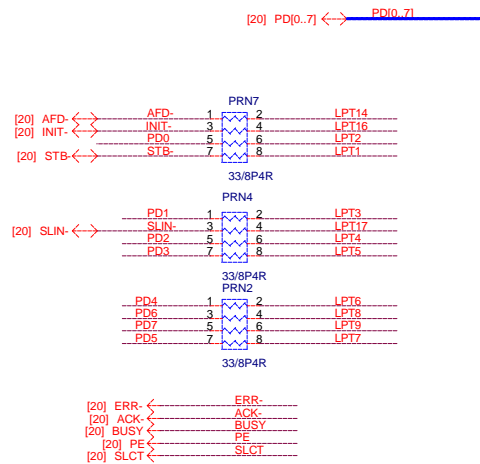
COMB



FLOPPY



PRT PORT



GIGABYTE CORP.

Title

COM & IR & LPT PORT & FLOOPY

Size

Document Number

GA-8ID2003-P

Rev

2.01

Date

Wednesday, April 02, 2003

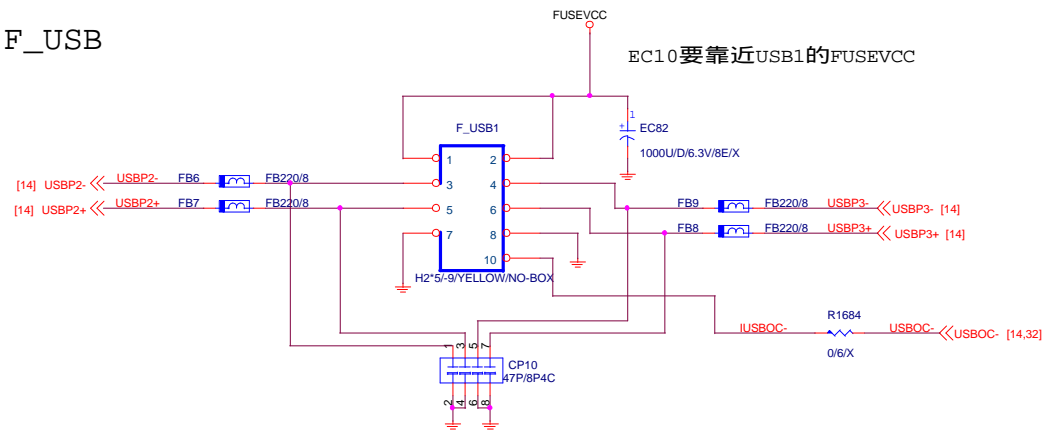
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23

of

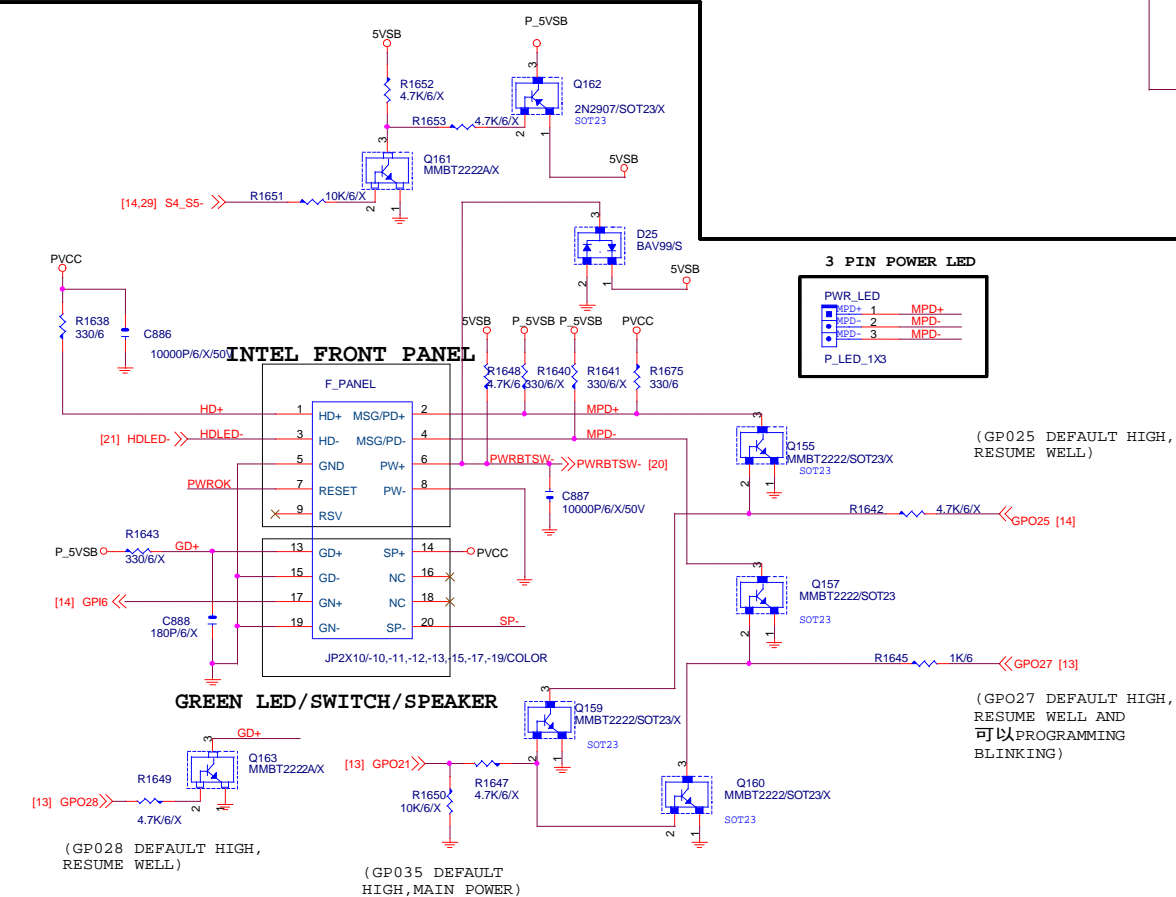
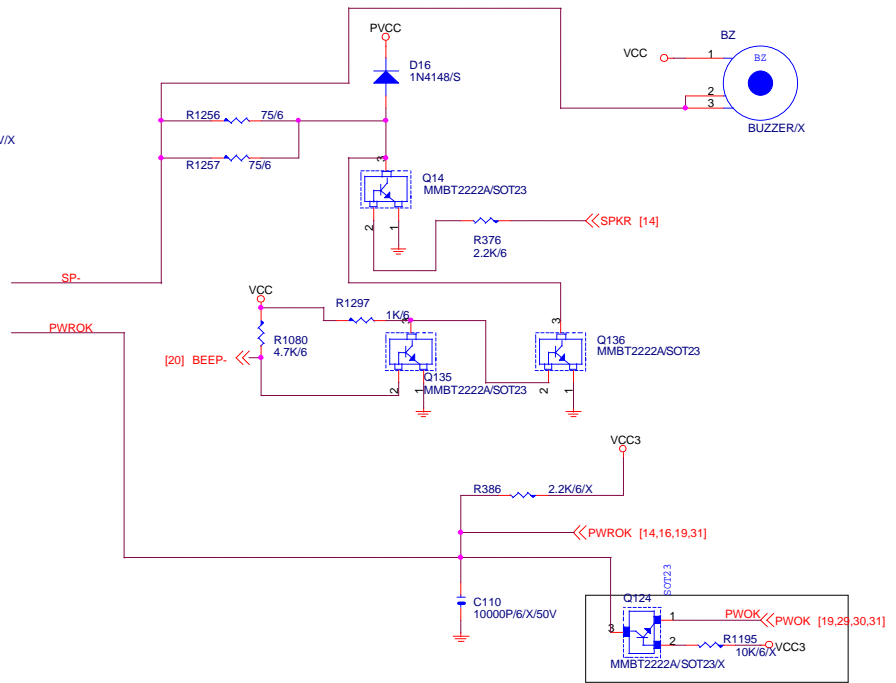
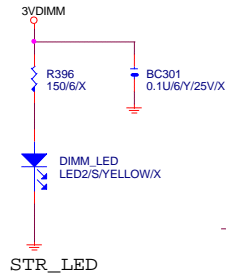
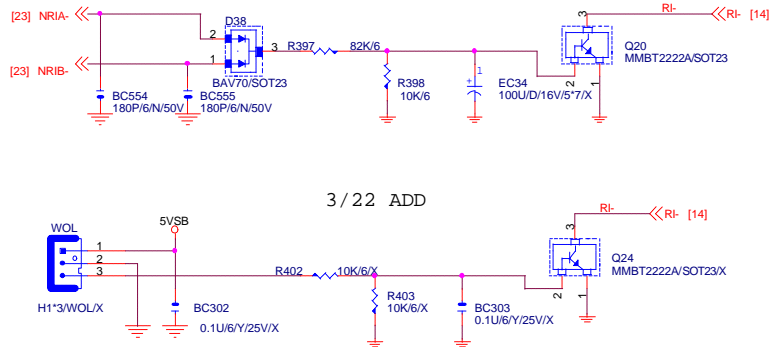
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F_USB

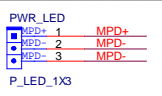


EC10要靠近USB1的FUSEVCC

GIGABYTE			
Title ICH USB+LAN PORT			
Size B	Document Number	Rev 2.01	
Date:	Wednesday, April 02, 2003	Sheet 24	of 34



3 PIN POWER LED



States for green LED

LED States	ACPI States	GP028
ON	S1, S3	0
OFF	S0, S5	1

NO1 GP035 只需在S1 PROGRAMMING LOW

States for a single-color power LED

LED States	ACPI States	GP025	GP027	GP035
OFF	S1, S3, S5	1	1	NO1
Steady Green	S0	0	1	0
Blinking Green	S0(message waiting)	0	B	0

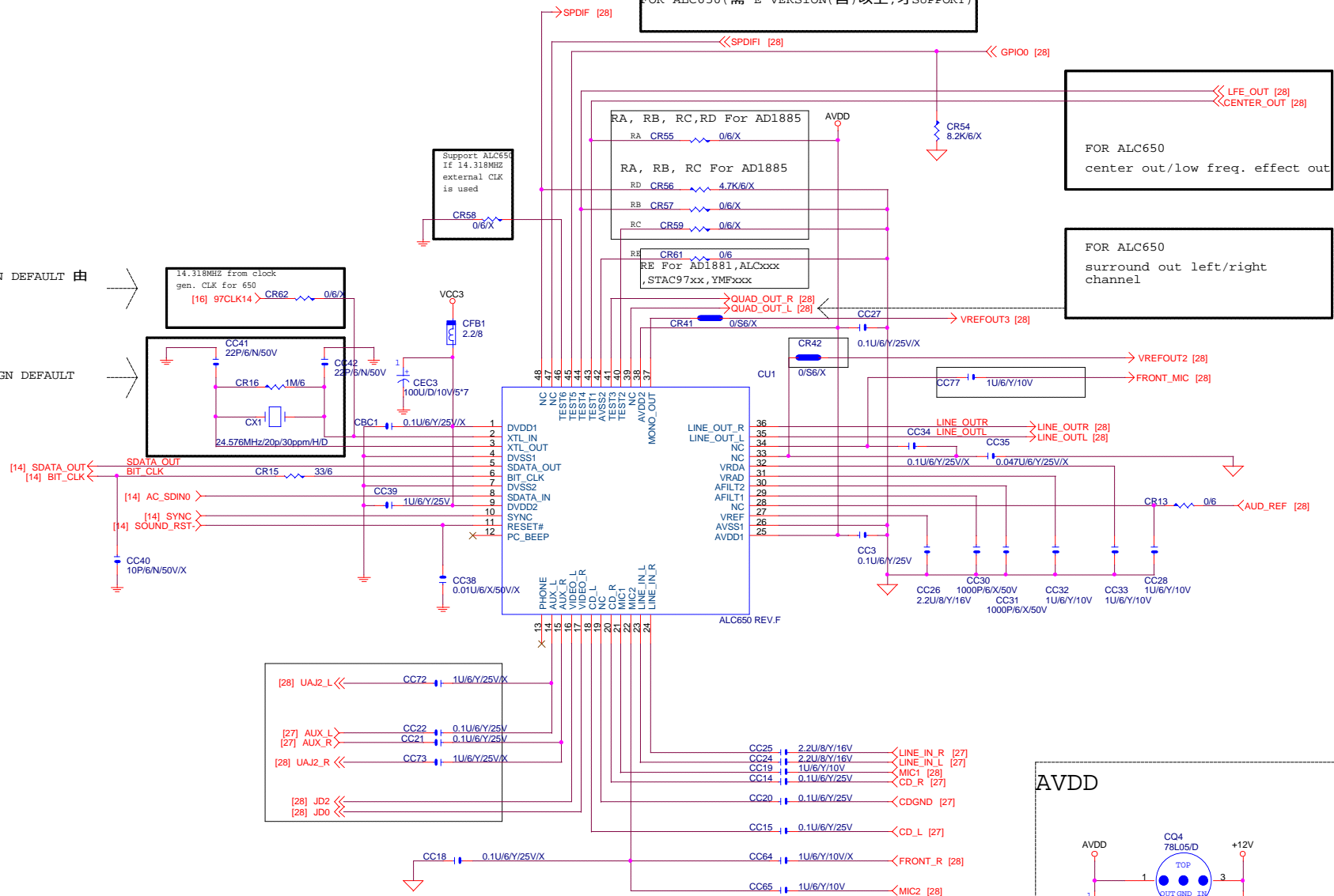
States for a dual-color power LED

LED States	ACPI States	GP025	GP027	GP035
OFF	S5	1	1	X
Steady Green	S0	0	1	0
Blinking Green	S0(message waiting)	0	B	0
Steady Yellow	S1, S3	1	0	NO1
Blinking Yellow	S1, S3(message waiting)	1	B	NO1

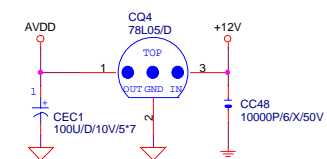
模組化線路

FOR 650 DESIGN DEFAULT 由
CLOCK GEN來

FOR 650 DESIGN DEFAULT
不上CRYSTAL



AVDD

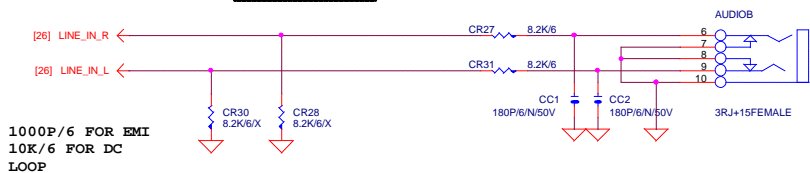


GIGABYTE CORP.

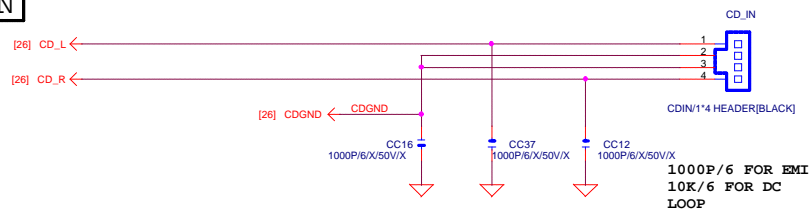
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Size	Document Number	Rev	
B	GA-8ID2003-P	2.01	
Date:	Wednesday, April 02, 2003	Sheet	26 of 34

LINE-IN

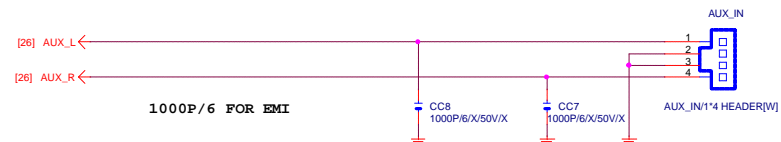
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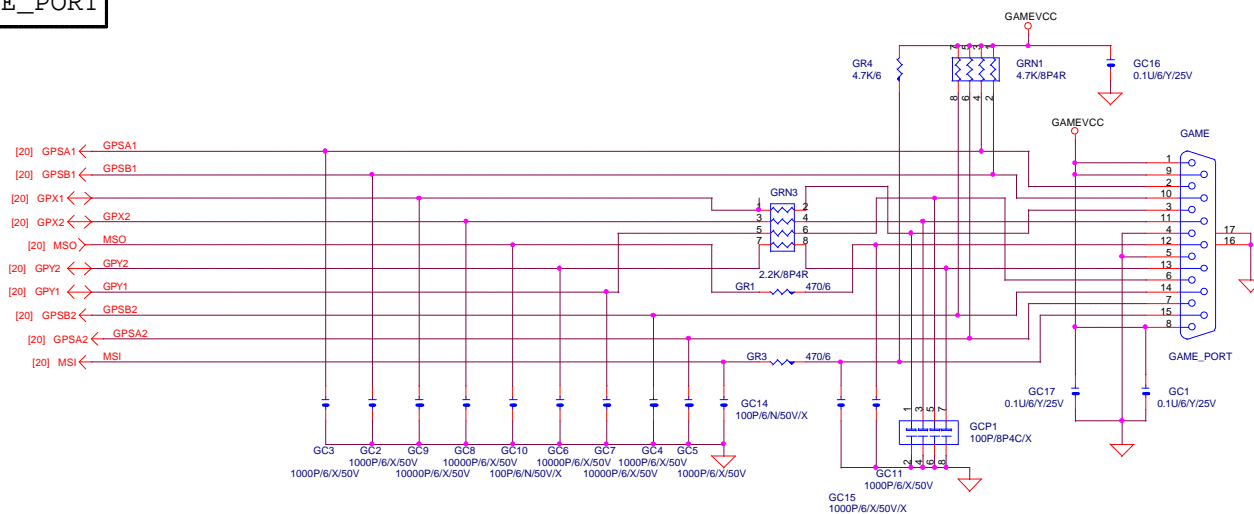
CDIN



AUX_IN

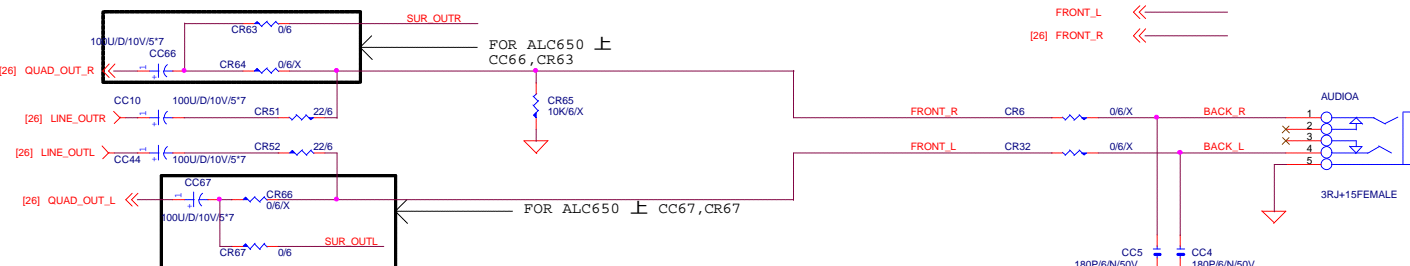


GAME_PORT

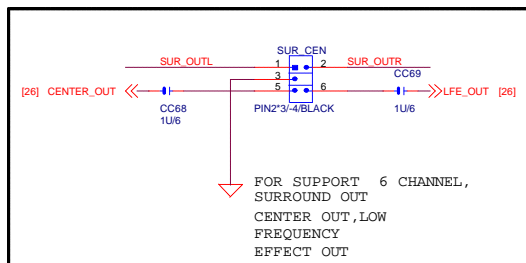
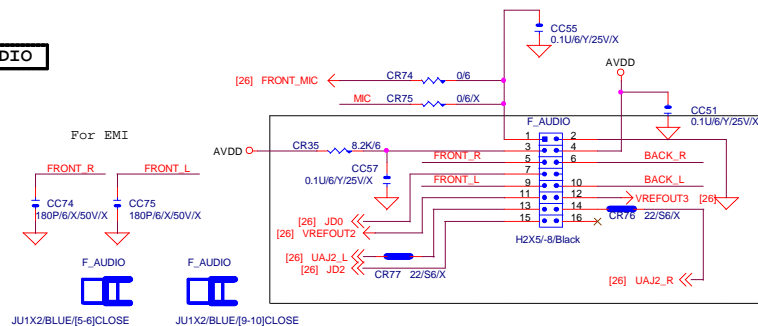


模組化線路

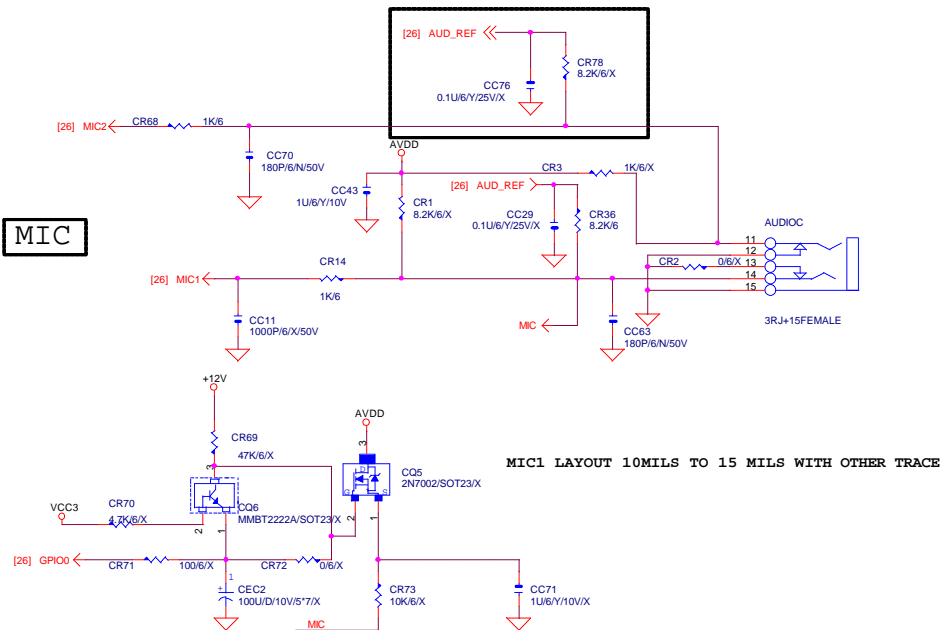
LINE OUT



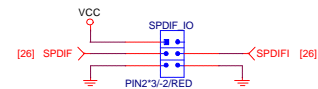
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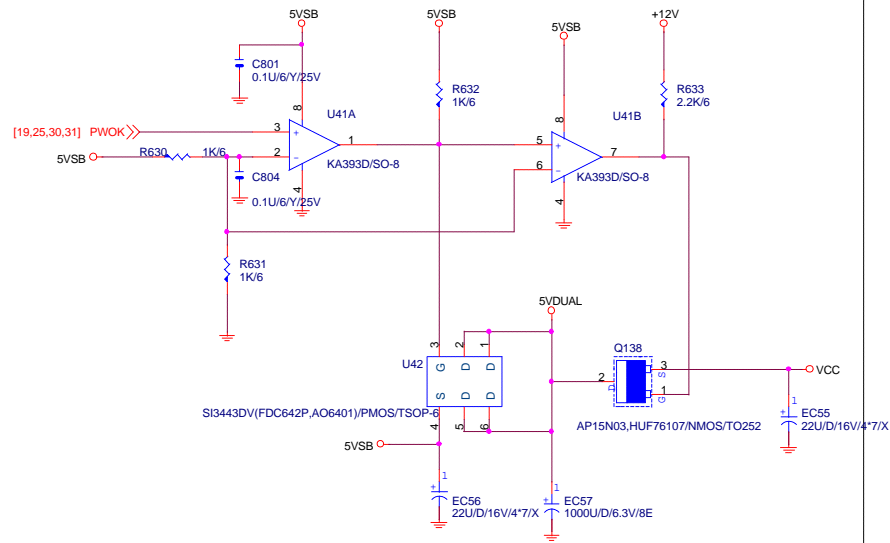
MIC



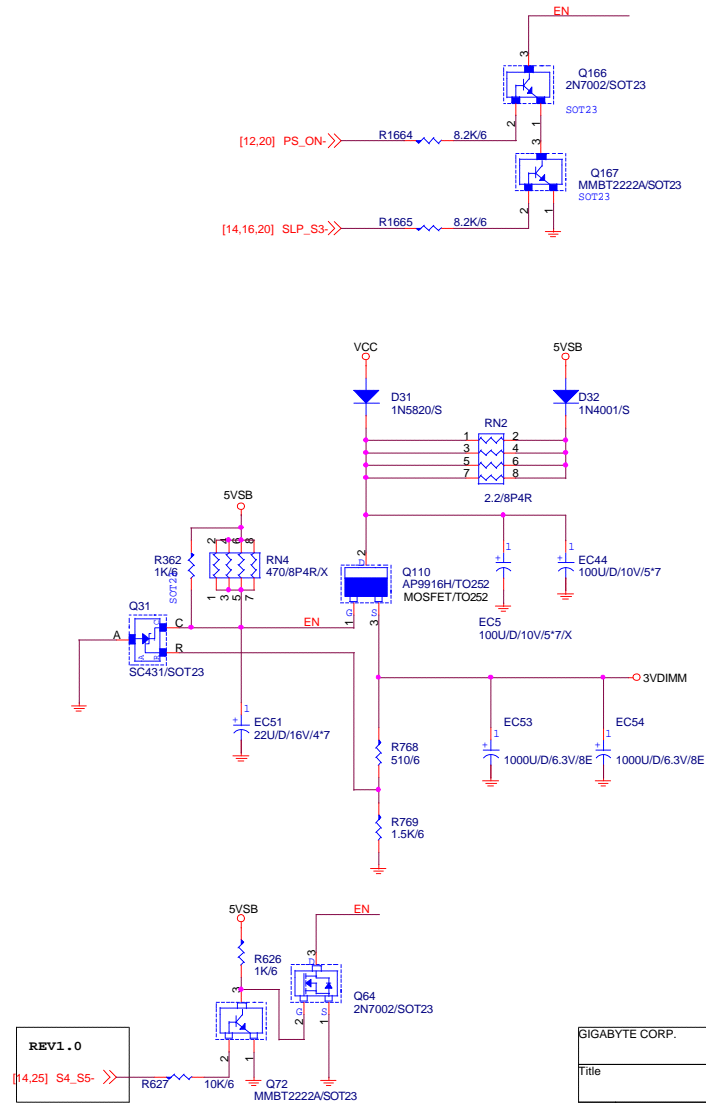
SPDIF_IO



SIOGABYTE CORP.			
Title			
PHONE JACK, GAME PORT			
Size	Document Number	Rev	
Custom	GA-8ID2003-P	2.01	
Date	Wednesday, April 02, 2003	Sheet	28 of 34



5VDUAL CIRCUIT



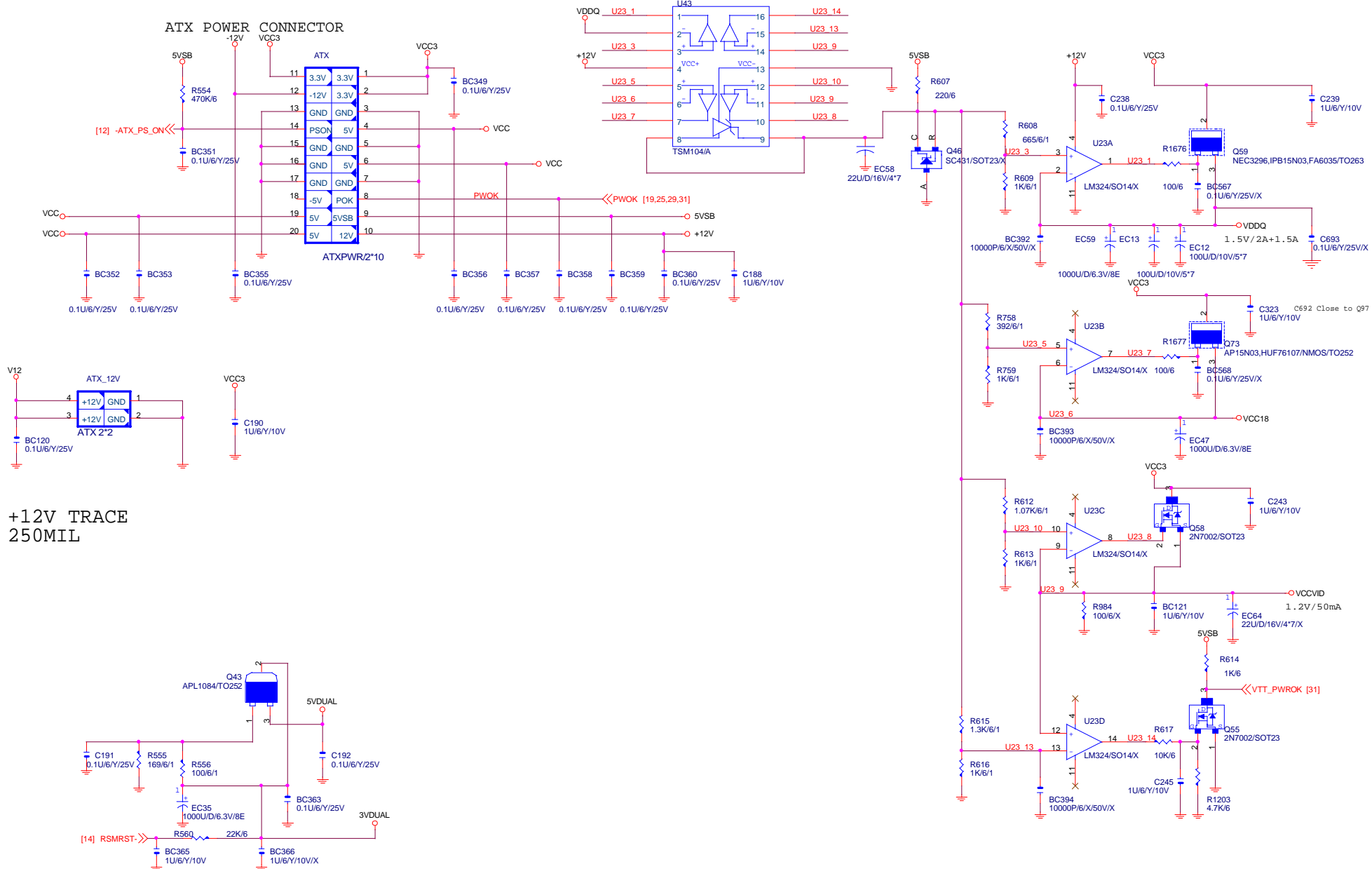
REV1.0

[14,25] S4_S5- >>

GIGABYTE CORP.		
Title		
DDR POWER		
Size	Document Number	Rev
B	GA-8ID2003-P	2.01
Date: Wednesday, April 02, 2003		
Sheet 29 of 34		

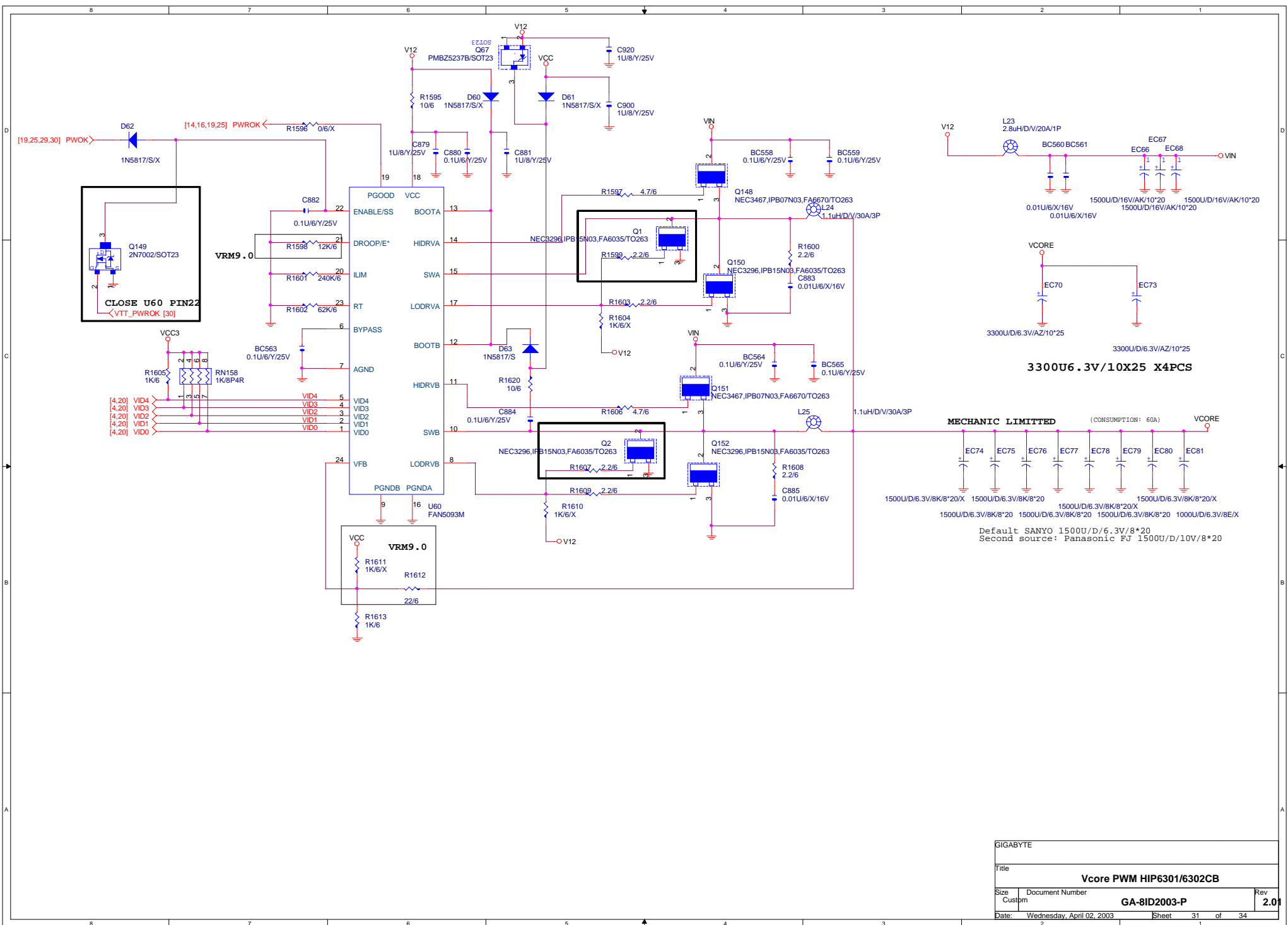
ATX POWER CONNECTOR

COLAYOUT (與U23)

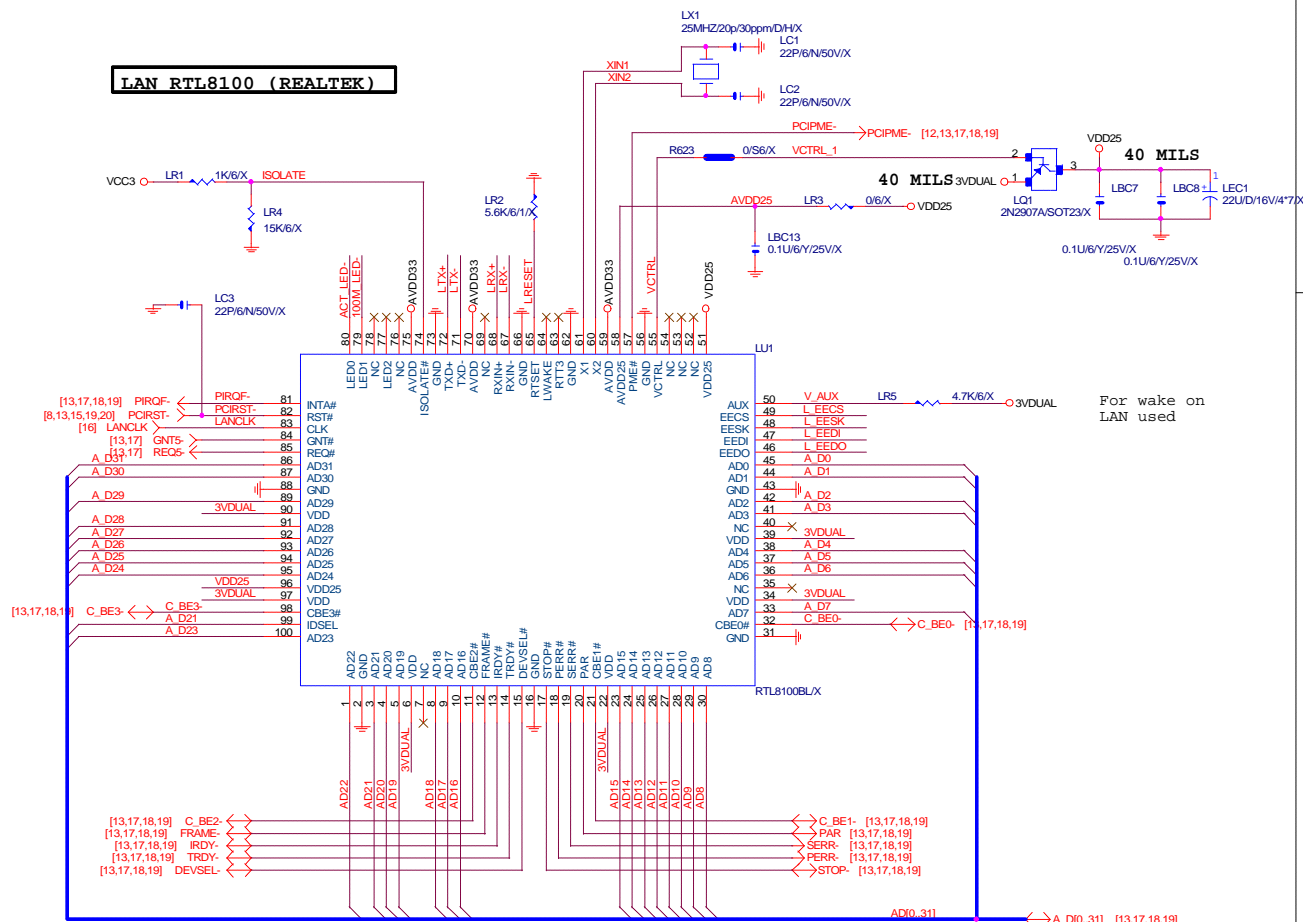


+12V TRACE
250MIL

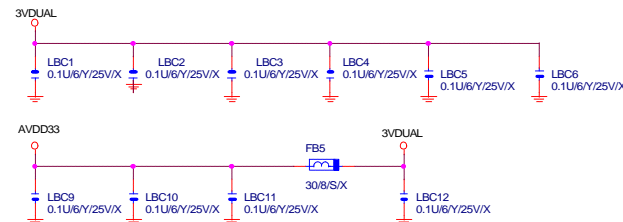
GIGABYTE CORP.			
Title			
Misc. PWR & ATX CONN.			
Size	Document Number		Rev
B	GA-8ID2003-P		2.01
Date:	Wednesday, April 02, 2003	Sheet	30 of 34



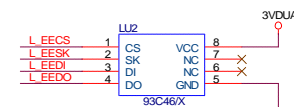
LAN RTL8100 (REALTEK)



POWER DECOUPLING CAP.



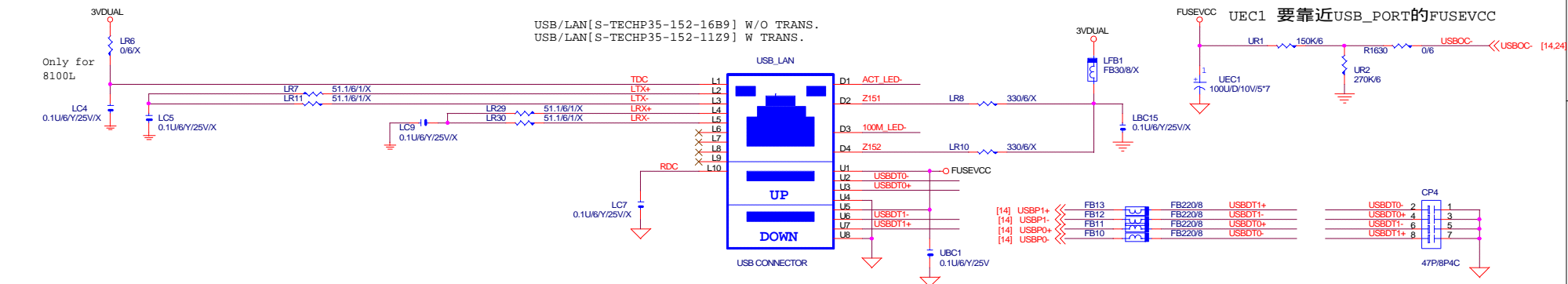
LAN EEPROM



8100/8100B NOTE:

Pin 65 pull-down (5.6K for 8100B)
Pin 65 pull-down (1.8K for 8100)
R502 remove pull-down (1.8K for 8100B)
R502:0/6 pull-down (1.8K for 8100)

USB/LAN[S-TECHP35-152-16B9] W/O TRANS.
USB/LAN[S-TECHP35-152-1129] W TRANS.



GIGABYTE

LAN RTL8100B

Title		Rev	
Size		2.01	
Document Number		GA-8ID2003-P	
Date: Wednesday, April 02, 2003		Sheet 32 of 34	

GIGABYTE GA-8ID2003-P
GPIO LIST

Revision 2.01

SHEET TITLE

GPI		
GPI0/REQA-	NA	PULL 8.2K TO VCC3
GPI1/REQ5-		PULL 2.7K TO VCC
GPI2/PIRQE-	ID	PULL 8.2K TO VCC3 (default)
GPI3/PIRQF-		PULL 8.2K TO VCC3
GPI4/PIRQG-		PULL 8.2K TO VCC3
GPI5/PIRQH-	ID	PULL 8.2K TO VCC3 (default)
GPI6		PULL 8.2K TO VCC3(GREEN BUTTON)
GPI7		PULL DOWN 10K TO GND
GPI8	ID	PULL 8.2K TO VCC3 (default)
GPI9	NA	NOT IMPLEMENTED
GPI10	NA	NOT IMPLEMENTED
GPI11		PULL 4.7K TO 3VDUAL (SMBALERT)
GPI12/LPCPME-		PULL 8.2K TO 3VDUAL (PME-)
GPI13		NC
GPI14	NA	NOT IMPLEMENTED
GPI15	NA	NOT IMPLEMENTED

SHEET TITLE

GPO		
GPO16	NA	PULL 8.2K TO VCC3(CAN'T PULL DOWN)因為會decode bios address 不正確:FF
GPO17		PULL 8.2K TO VCC3 (GNT5-)
GPO18	NA	PULL 8.2K TO VCC3 此OUTPUT 會TOGGLE.(在開機時).BIOS POST
GPO19		OK PULL 8.2K TO VCC3
GPO20		PULL 8.2K TO VCC3
GPO21		PULL 8.2K TO VCC3 (TOP BLOCK)
GPO22		PULL 8.2K TO VCC3
GPO23		PULL 8.2K TO VCC3
GPO24		PULL 4.7K TO 3VDUAL
GPO25		PULL 4.7K TO 3VDUAL (POWER LED)
GPO26		NOT IMPLEMENTED
GPO27		PULL 8.2K TO 3VDUAL (POWER LED)
GPO28		PULL 8.2K TO 3VDUAL
GPO32		PULL 8.2K TO 3VDUAL (BIOS WRITE PROTECT)
GPO35		PULL DOWN 10K TO GND (POWER LED)

COMPONENT SIDE
(0.5 oz. Copper)
VCC SIDE
(1 oz. Copper)
GND SIDE
(1 oz. Copper)
SOLDER SIDE
(0.5 oz. Copper)

GIGABYTE

Title

GPIO LIST

Size Custom

Document Number

GA-8ID2003-P

Rev

2.01

Date: Wednesday, April 02, 2003

Sheet 33 of 34

1

Revision 2.01

TITLE

[illegible]

TITLE

[illegible]

		COMPONENT SIDE		A
<input type="text"/>		(0.5 oz. Copper)		
<input type="text"/>		VCC SIDE		
<input type="text"/>		(1 oz. Copper)		
<input type="text"/>		GND SIDE		
<input type="text"/>		(1 oz. Copper)		
<input type="text"/>		SOLDER SIDE		
<input type="text"/>		(0.5 oz. Copper)		
GIGABYTE				
Title				
PCI ROUNT LIST				
Size	Document Number			Rev
Custom	G4-8ID2003-P			2.01
Date:	Wednesday, April 20, 2003		Sheet	34 of 34