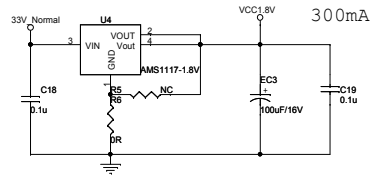
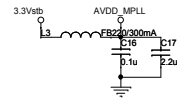
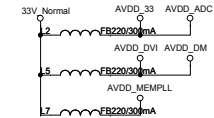
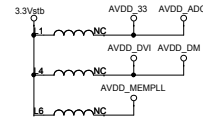
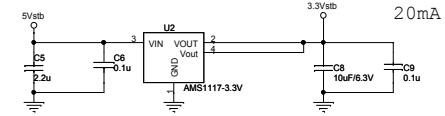
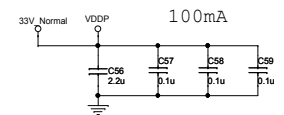
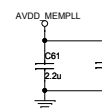
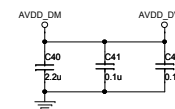
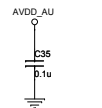
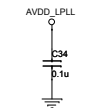
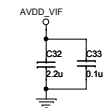
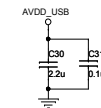
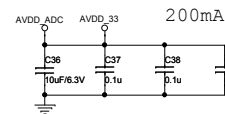
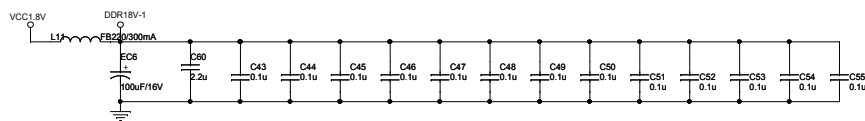
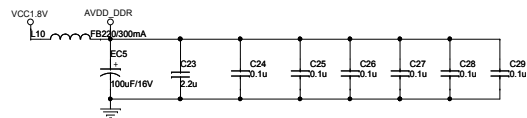
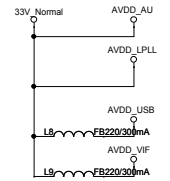
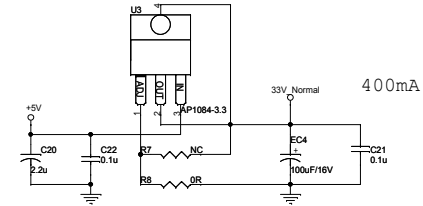


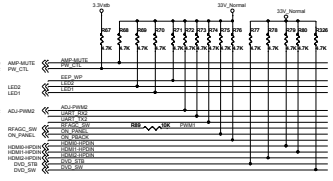
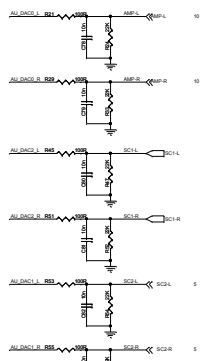
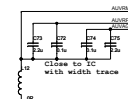
### 3.3V Power Standby only for AVDD\_MPLL and IR SAR,CEC.GPIO.RST



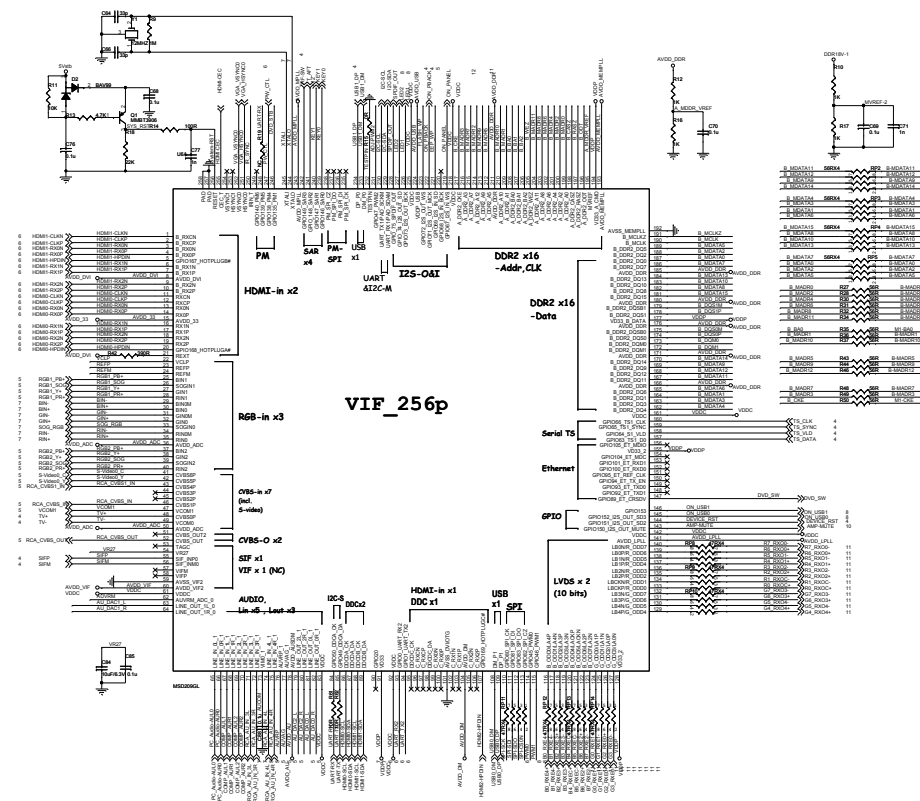
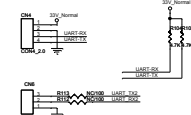
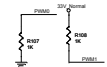
### 3.3V Power\_Normal



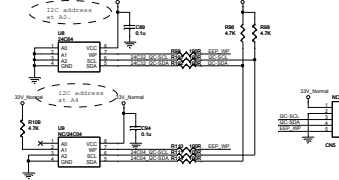
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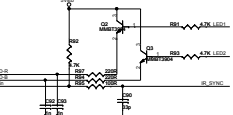
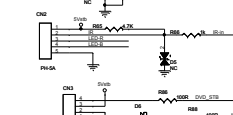
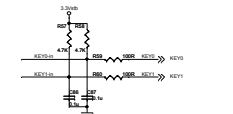
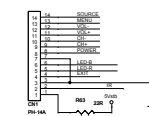
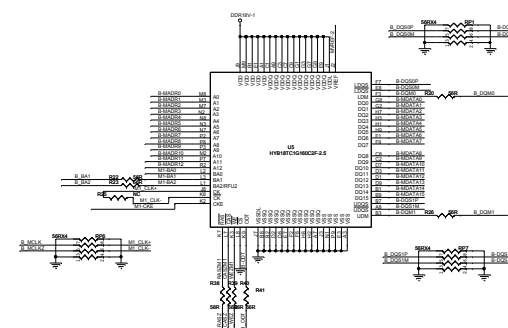
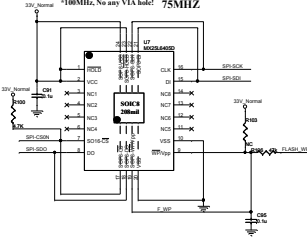
# IC Configuration Selection



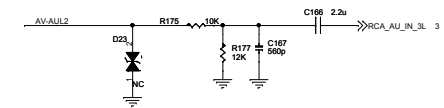
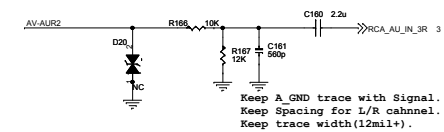
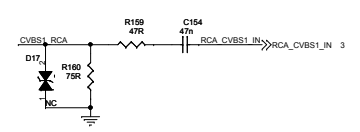
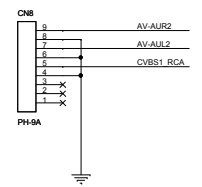
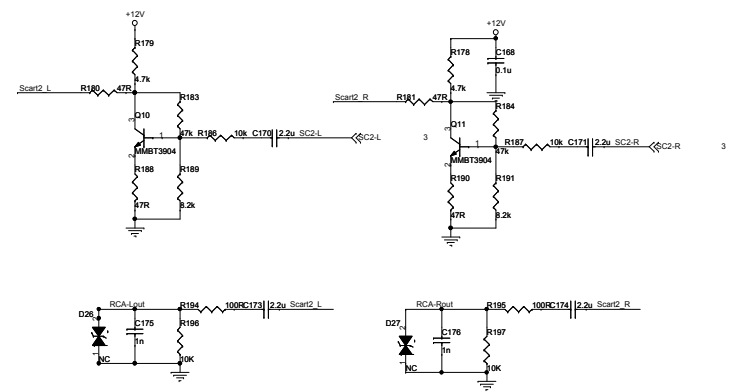
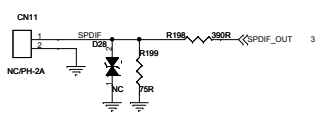
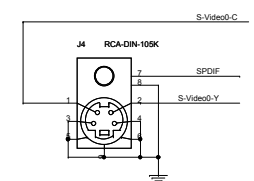
## EEPROM



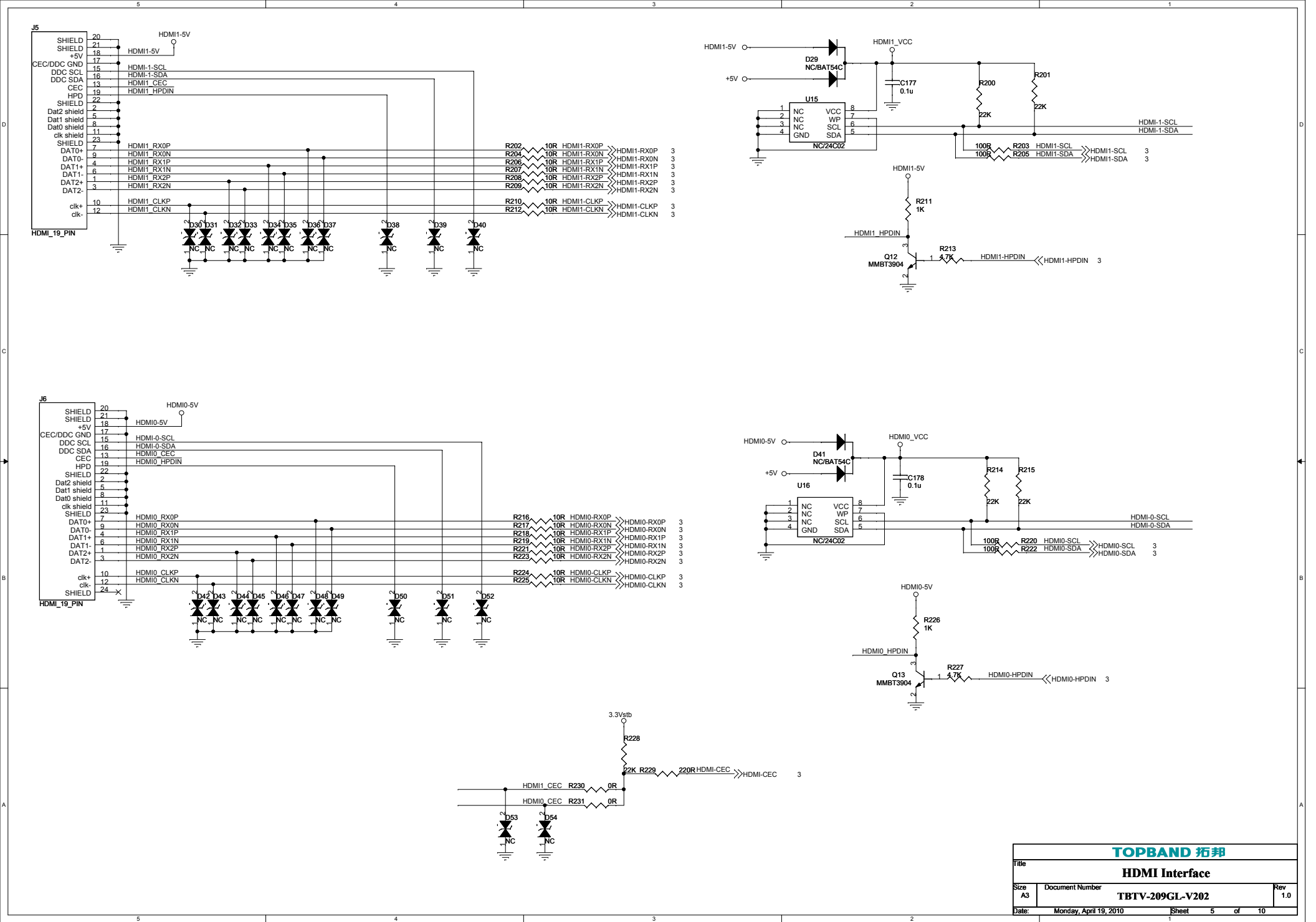
## Boot Loader SPI Flash

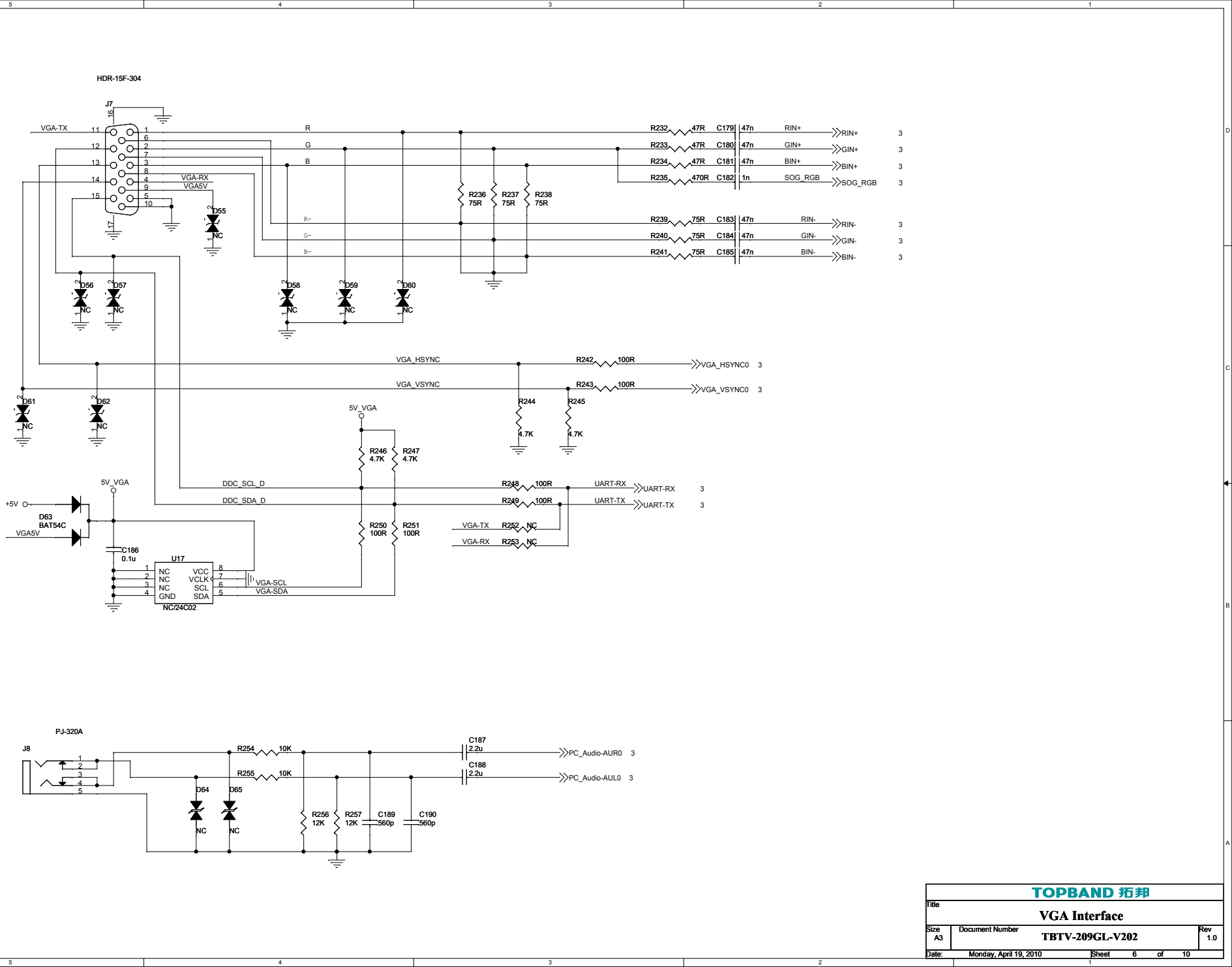




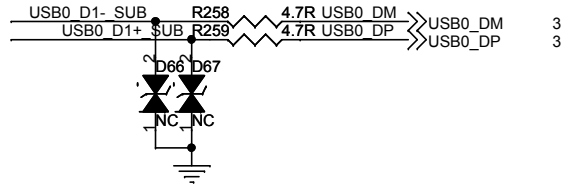
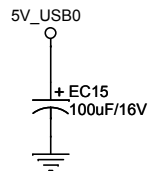
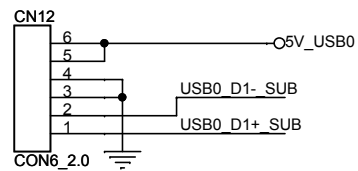


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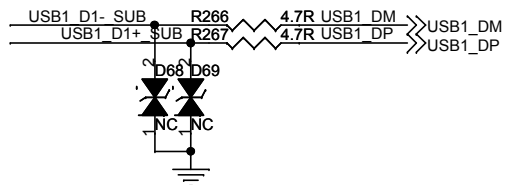
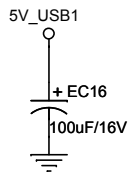
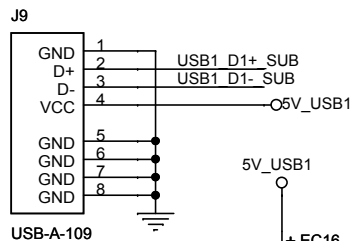
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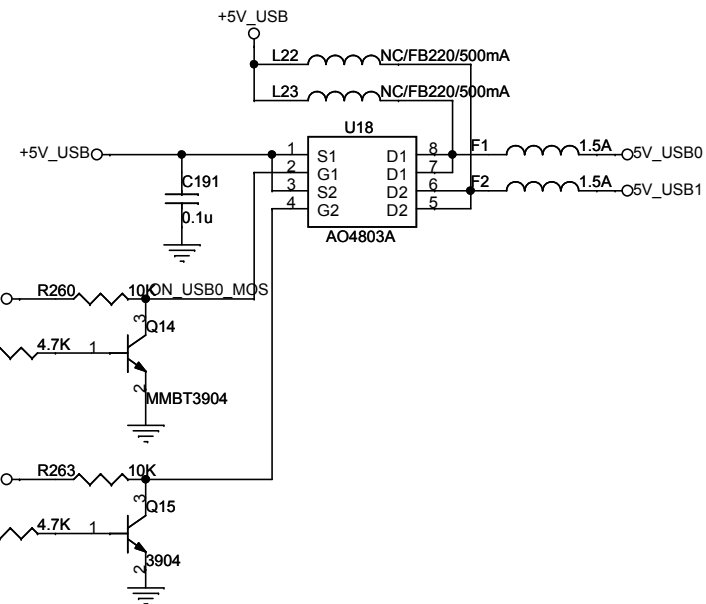
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3

33V\_Normal  
ON\_USB0

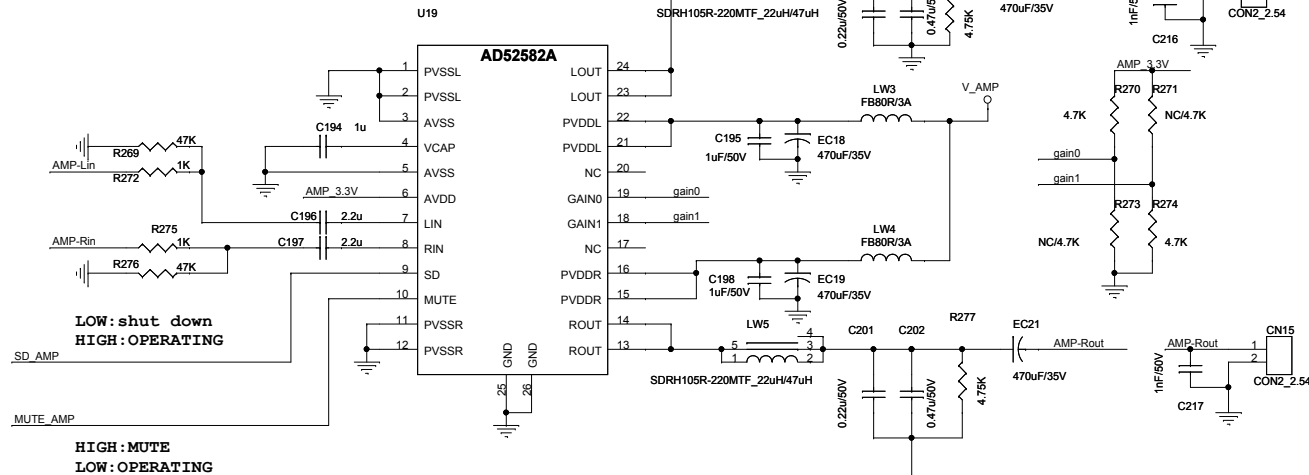
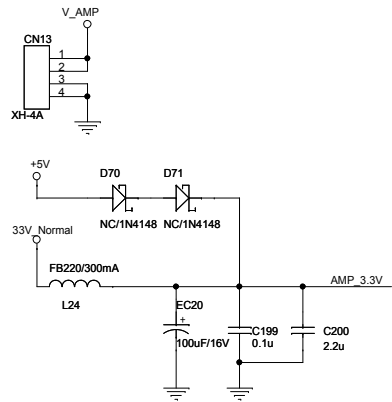
3  
ON\_USB1



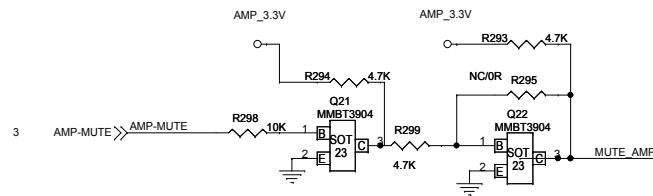
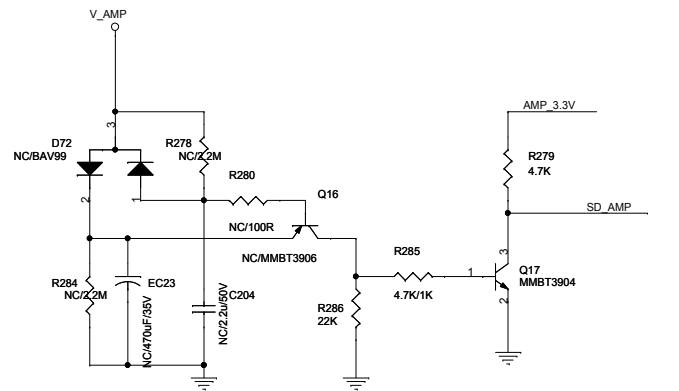
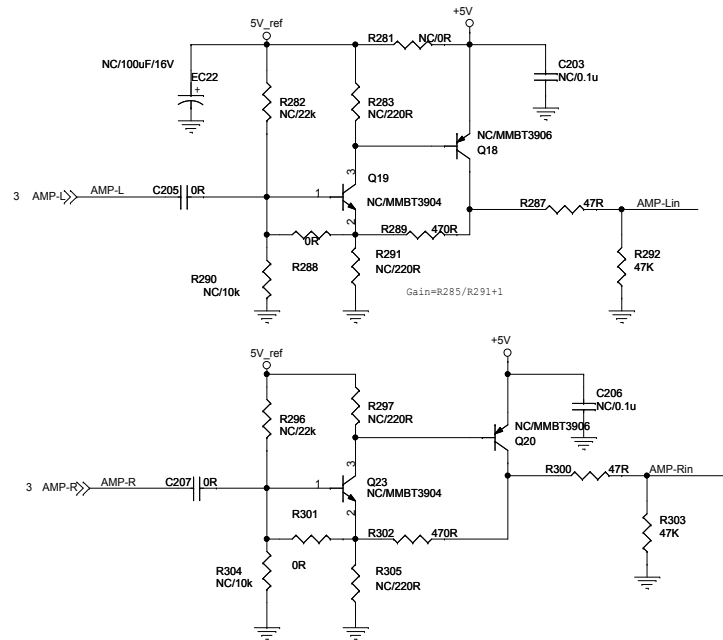
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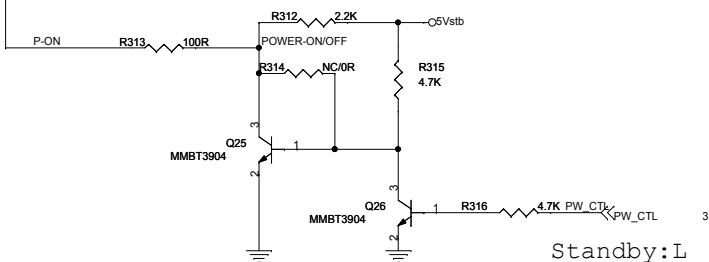
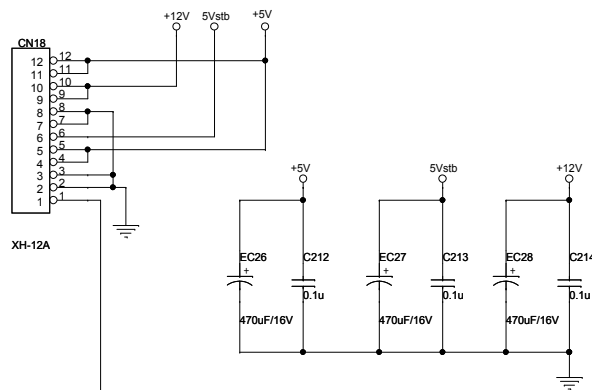
## AUDIO OP



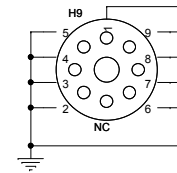
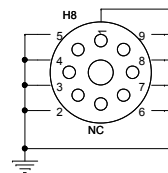
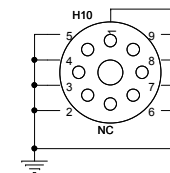
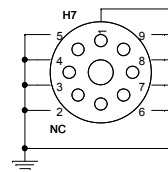
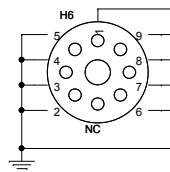
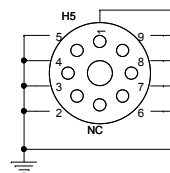
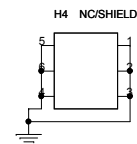
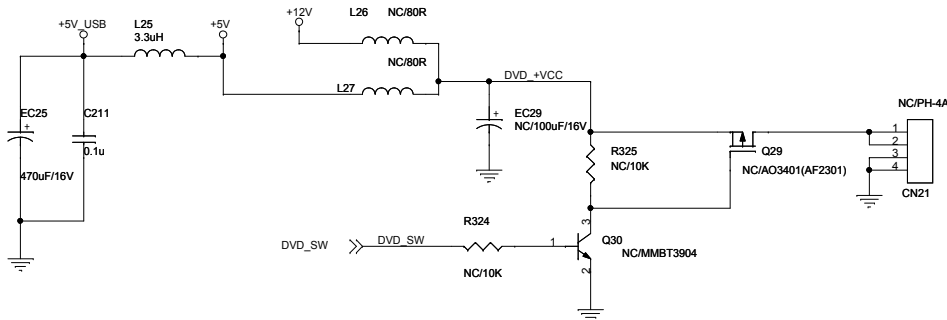
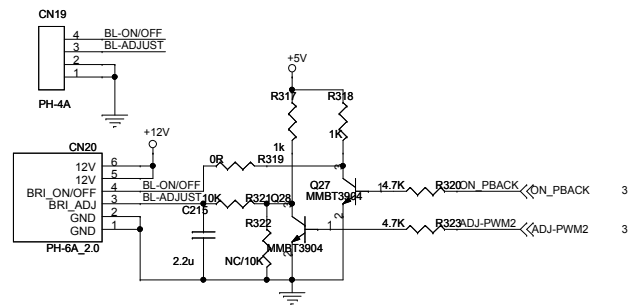
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Standby:L  
Normal: H



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